

ORDER, NO. **ARP2058** 



## MODEL PD-93 HAS FOLLOWING VERSIONS:

Туре	Power requirement	Export destination
KU/CA	AC120V only	U.S.A. and Canada
HEM	AC220V, 240V (switchable) *	European continent

\*Change the primary wiring of the power transformer.

- ●This manual is applicable to the KU/CA and HEM types.
- ◆Ce manuel pour le service comprend les explications de réglage en français.
- Este manual de servicio trata del método ajuste escrito en español.

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This service manual is intended for qualified service technicians; it is not meant for the casual do-it-yourselfer. Qualified technicians have the necessary test equipment and tools, and have been trained to properly and safely repair complex products such as those covered by this manual.

Improperly performed repairs can adversely affect the safety and reliability of the product and may void the warranty. If you are not qualified to perform the repair of this product properly and safely, you should not risk trying to do so and refer the repair to a qualified service technician.

#### WARNING

Lead in solder used in this product is listed by the California Health and Welfare agency as a known reproductive toxicant which may cause birth defects or other reproductive harm (California Health & Safety Code, Section 25249.5).

When servicing or handling circuit boards and other components which contain lead in in solder, avoid unprotected skin contact with the solder. Also, when soldering do not inhale any smoke or fumes produced.

## 1. SAFETY INFORMATION

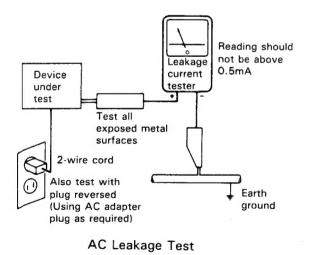
### -(FOR USA MODEL ONLY)-

### 1. SAFETY PRECAUTIONS

The following check should be performed for the continued protection of the customer and service technician.

### LEAKAGE CURRENT CHECK

Measure leakage current to a known earth ground (water pipe, conduit, etc.) by connecting a leakage current tester such as Simpson Model 229-2 or equivalent between the earth ground and all exposed metal parts of the appliance (input/output terminals, screwheads, metal overlays, control shaft, etc.). Plug the AC line cord of the appliance directly into a 120V AC 60Hz outlet and turn the AC power switch on. Any current measured must not exceed 0.5mA.



ANY MEASUREMENTS NOT WITHIN THE LIMITS OUTLINED ABOVE ARE INDICATIVE OF A POTENTIAL SHOCK HAZARD AND MUST BE CORRECTED BEFORE RETURNING THE APPLIANCE TO THE CUSTOMER.

### 2. PRODUCT SAFETY NOTICE

Many electrical and mechanical parts in the appliance have special safety related characteristics. These are often not evident from visual inspection nor the protection afforded by them necessarily can be obtained by using replacement components rated for voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in this Service Manual.

Electrical components having such features are identified by marking with a  $\triangle$  on the schematics and on the parts list in this Service Manual.

The use of a substitute replacement component which dose not have the same safety characteristics as the PIONEER recommended replacement one, shown in the parts list in this Service Manual, may create shock, fire, or other hazards.

Product Safety is continuously under review and new instructions are issued from time to time. For the latest information, always consult the current PIONEER Service Manual. A subscription to, or additional copies of, PIONEER Service Manual may be obtained at a nominal charge from PIONEER.

### (FOR EUROPEAN MODEL ONLY) -

VARO!

AVATTAESSA JA SUOJALUKITUS OHITETTAESSA OLET ALTTIINA NÄKYMÄTTÖMÄLLE LASERSÄTEILYLLE. ÄLÄ KATSO SÄTEESEEN.

ADVERSEL: -

USYNLIG LASERSTRÅLING VED ÅBNING NÅR SIKKERHEDSAFBRYDERE ER UDE AF FUNKTION UNDGÅ UDSAETTELSE FOR STRÅLING.

VARNING! -

OSYNLIG LASERSTRÅLNING NÄR DENNA DEL ÄR ÖPPNAD OCH SPÄRREN ÄR URKOPPLAD. BETRAKTA EJ STRÅLEN.



LASER Kuva 1 Lasersateilyn varoitusmerkki

- WARNING! -

DEVICE INCLUDES LASER DIODE WHICH EMITS INVISIBLE INFRARED RADIATION WHICH IS DANGEROUS TO EYES. THERE IS A WARNING SIGN ACCORDING TO PICTURE 1 INSIDE THE DEVICE CLOSE TO THE LASER DIODE.



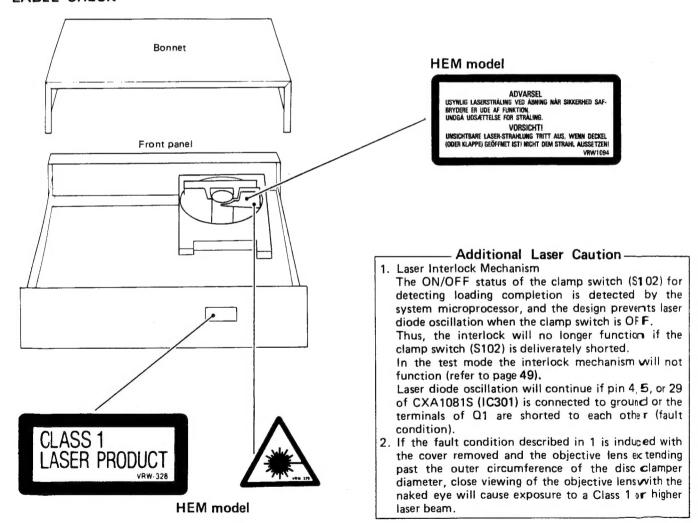
LASER
Picture 1
Warning sign for laser radiation

- IMPORTANT -

THIS PIONEER APPARATUS CONTAINS LASER OF HIGHER CLASS THAN 1. SERVICING OPERATION OF THE APPARATUS SHOULD BE DONE BY A SPECIALLY INSTRUCTED PERSON.

LASER DIODE CHARACTERISTICS -MAXIMUM OUTPUT POWER: 5 mw WAVELENGTH: 780-785 nm

### LABEL CHECK



## 2. DISASSEMBLY

### • REMOVAL OF FRONT PANEL

- Keep the side boards (L) and (R) apart from the upper plate by loosing the screws ((L, 4) (R, 4)) fixing them. (Be sure not to remove yet since the lead wire for ground is attached to the side sash on the side board (L) and (R).
- Remove the upper plate. (Remove the four black screws (upper side) and four copper screws (rear side).)
- 3. Remove the screws (A) and (A) fixing the ground lead wire for side sash. (Refer to Fig. (2-1))
- 4. Remove the side boards (L) and (R).
- 5. Turn the power to draw out the tray. (Refer to Note 1 when opening the tray manually.)
- 6. Remove the tray name plate. (Loosen the screw (B) and (B2) enough.
- 7. Remove the tray fixing plate (Screws (1), (2))
- 8. Remove the front panel (Remove the three screws of upper side and the four screws of lower side.)

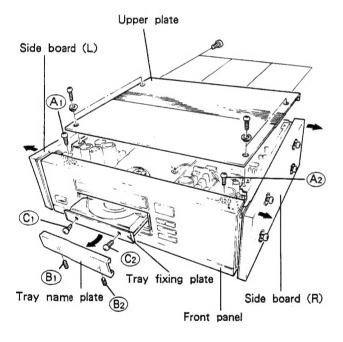


Fig. 2-1.

## Note 1. How to open the tray manually

- 1. Loosen the screw @ fixing the clamp motor.
- Taking care not to drop the iron ball of the tip
  of the gear section of the clamp motor as shown
  in Fig. 2-2, keep the engaging section @ of gear
  apart from the unit by tilting the clamp motor
  in the direction of arrow.
- 3. Turn the clamp cam counterclockwise to the position where the leaf switch turns on. (Set to the state that the clamp holder is raised.)
- Mount the clamp motor again. (Drive the screw E.)
- 5. Push the tray from behind to open it.

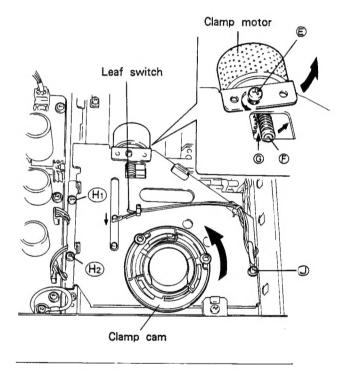


Fig. 2-2.

### ■ REMOVAL OF TRAY ASSEMBLY

- Remove the upper plate as in the steps 1 and 2 of "REMOVAL OF FRONT PANEL".
- 2. Remove by turning over the clamp mechanism assembly. (Screws H), H2 and J, Refer to Fig. 2-2)
- Remove the fixing screws (K1) and (K2) of the tray assembly and remove the slide base from the slider unit of the loading mechanism assembly by pushing the claw (D. (Refer to Fig. 2-3).
- 4. Remove the tray assembly by drawing out from the front panel.

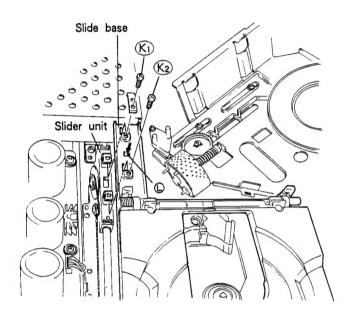
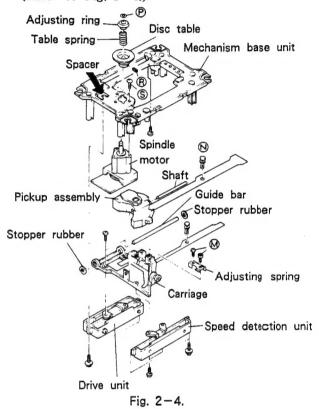


Fig. 2-3.

### ■ REMOVAL OF PICKUP ASSEMBLY

- Remove the upper plate as in the steps 1 and 2 of "REMOVAL OF FRONT PANEL".
- 2. Remove by turning over the clamp mechanism assembly. (Screws (H)), (H2) and (J), Refer to Fig. 2-2.)
- 3. Move the tray to the open position, (Refer to Note 1.)

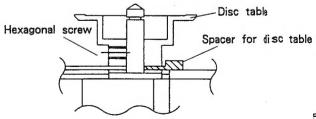


## • INSTALLATION OF DISC TABLE

Cut the spacer shown by the arrow (refer to Fig. 2-4.) with a nipper (the rear side), enter it between the disc table and the mechanism base, and screw it. Torque over 5 kgcm to screw.

(Reference) In the case of no torque driver, tighten firmly the hexagonal screw, press the disc table from the upper side, and check that it does not slip down.

Remove the spacer after installing the disc table. (The spacer is  $1^{+0}_{0.05}$  in thickness.)





### • REMOVAL OF SPINDLE MOTOR

- 1. Remove the base (alias bottom plate) (17 screws).
- Remove the split washer ₱ fixing the adjusting ring. (Refer to Fig. 2-5.)
   (Remove it with a tweezers while pressing the adjusting ring??.)
- 3. Move the tray to the open position. (Refer to Note 1.)
- 4. Loose the screws ® of the disc table from the opening part of the front panel with the hexagonal driver and remove the disc table.
- 5. Remove the fixing screws (3) of the spindle motor
- 6. Remove the lead wires (7) wired as shown in Fig. 2-5, with a soldering iron.

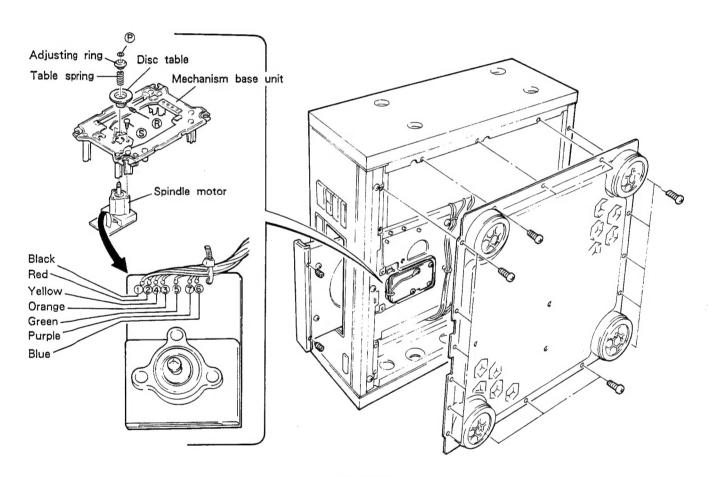
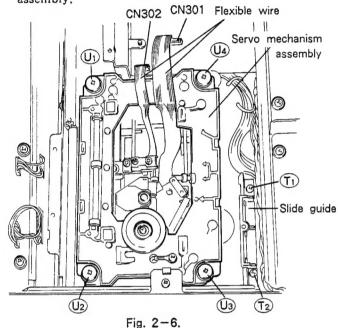


Fig. 2-5.

# ● REMOVAL OF SERVO MECHANISM ASSEMBLY

- Remove the tray assembly. (Refer to REMOVAL OF TRAY ASSEMBLY.)
- 2. Remove the slide guide. (Screws T1) and T2, refer to Fig. 2-6.)
- 3. Remove the four screws (①) to ①) fixing the servo mechanism assembly.
- Remove the flexible wire from the CN301 and CN302, and remove the servo mechanism assembly.



## REMOVAL OF LOADING MOTOR

1. Remove the loading mechanism assembly.

### REMOVAL OF LOADING MECHANISM ASSEMBLY

- Remove the servo mechanism assembly. (Refer to REMOVAL OF SERVO MECHANISM ASSEMBLY.)
- 2. Remove the screws (v) and (v) fixing the loading mechanism assembly and the lead wire from cord stopper (v). (Refer to Fig. 2-7.)
- Move the loading mechanism assembly backward a little and remove it by raising the end of front panel.

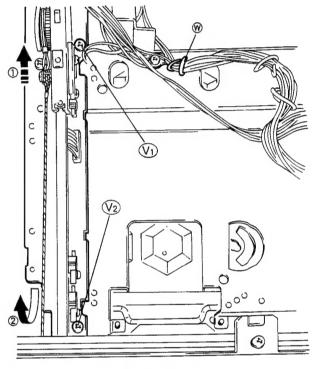
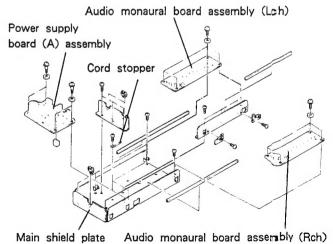


Fig. 2-7.

## 3. OPERATION CHECK OF MAIN BOARD ASSEMBLY

- Remove the three assemblies of the audio monaural boards (Lch), (Rch) and the power supply board (A). (Remove for the output pin jack portion with a soldering iron.)
- 2. Remove the main shield plate (6 screws). It is possible to turn on the power and check the servo circuit in this state. (Refer to Fig. 3-1 for the position to install each assembly mentioned above.)



.

Fig. 3-1.

## 4. EXPLODED VIEWS AND PARTS LIST

### NOTES:

- Parts without part number cannot be supplied.
- The  $\triangle$  mark found on some component parts indicates the impotance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- Parts marked by "O" are not always kept in stock. Their delivery time may be longer than usual or they may be unavailable.

## 4.1 Parts List of Exterior(1)

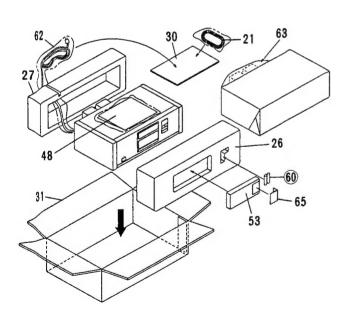
Mark	No.	Symbol & Description	Part No.	Mark	No.	Symbol & Description	Part No.
	1	Name plate	AAM1001		41	Tray fixing plate	PNS1008
	2	LED lens	AMR1160		42	Clamp knob	PNW1236
	3	Screw	AMZ40P180FRD		43	Joint	
	4	Screw	BBT30P080FCU		44	Wood collor	PNW1238
	5	Screw	BBZ30P060FCC		45	Lens(A)	PNW1460
		4					
	6	Screw	BBZ30P080FRD		46	Control panel	PNW1579
	7	Screw	BBZ30P080FZK		47	Power button	PNW1580
	8	Screw	IBZ30P080FCC		48	Operating instructions	PRB1121(KU/CA type)
	9	Screw	IBZ30P080FCC			(English)	
	10	Select button	PAC1325			(English/French/German/ Italian/Dutch/Spanish/	PRE1117 (HEM type)
	11	Main button assembly	PAD1053			Swedish/Portugese)	
	12	FL sheet	PAM1290(KU/CA type)		49	Caution label	
			PAM1251 (HEM type)		50		
	13	Display window	PAM1344				
	14	Front panel	PAN1162(KU/CA type)		51	Clamp caution label	
		Tronc paner	PAN1161 (HEM type)	•	52	Main operation board	PWZ1742
	15	Side sash	PAN1151	©	JL	assembly	1 11/1/14/2
	10	Side Sasii	IANTISI		53	Remote control unit	PWW1045
	16	Trou rose plate	PAN1152		54	Leg assembly	AMR1159
	16	Tray name plate	PBA1017				WWW 1198
	17	Screw	PBATUTI		55	Servo mechanism assembly	
	18	m	••••				
	19	Plate spring A			56	Loarding mechanism assemb	ly
	20	Plate spring B			57	Tray assembly	
					58	Clamp mechanism assembly	
	21	Connection cord	PDE1032		59	Screw	RBA-093
	22	Cushion rubber			60	Battery	
	23	Vibration isolating rubber					
	24	Side rubber(R)	PEB1118		61	Washer	WA42N12OW050
	25	Side rubber(L)	PEB1119		62	Plastic bag	Z21-037
					63	Sheet	Z23-024
	26	Protector(F)	PHA1111		64	Screw	ZMD30H040FBT
	27	Protector(R)	PHA1112		65	Battery cover	PZN1007
	28	Spacer	PHC1018			•	
	29	Sheet	PHC1022		66	Sub operation board	
	30	Upper plate	PHC1047		••	assembly	
	00	opper plate	11101011		67	LED board assembly	
	31	Packing case	PHG1494(KU/CA type)		68	Screw	PYC30P100FMC
	31	I dealing case	PHG1493 (HEM type)		69	Play lens	PNW1258
	20	Collor	PLA1029		70		
	32				10	Stop rubber	PEB1140
	33	Side board(L)	PMM1019(KU/CA type)			m	<b>701</b> 010
			PMM1027 (HEM type)		71	Plastic bag	Z21-013
	34	Side board(R)	PMM1020(KU/CA type) PMM1028(HEM type)		72	Front panel assembly	PEA1090(KU/CA type) PEA1091(HEM type)
	35	Screw	PMZ40P060FMC			•	
	36	Base					
	37	Vibration isolating					
	•	material S					•
	38	Cushion					
	~						
	39	Nois absorption material	PNM1102				

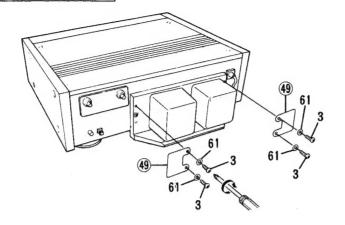
Mount the screws and knobs removed to the original positions.

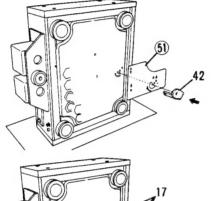
Perform in the reverse order of removals.

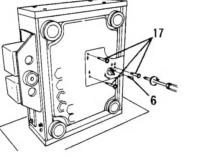
- 1. Mount the fixing screw for transformer on the rear side.
- 2. Mount the fixing knob and screw on the bottom side.
- ① Stand this mechanism sideways.
- ② Match the mark \( \triangle \) on the gray knob and the mark \( \triangle \) on the bottom side, and insert the knob. Insert so as to enter the pole of the bottom lid into the round hole of the knob's end.
- 3 Turn the knob counterclockwise.
- Match the screw holes, insert the screw and tighten it with a phillips screwdriver.
- 3. Remove the tray and insert the spacer for transport.

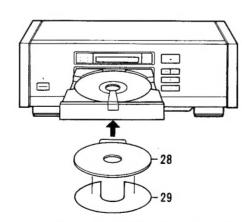
**Note:** Tighten the fixing knob and screw of the bottom lid, before inserting the spacer for transport.



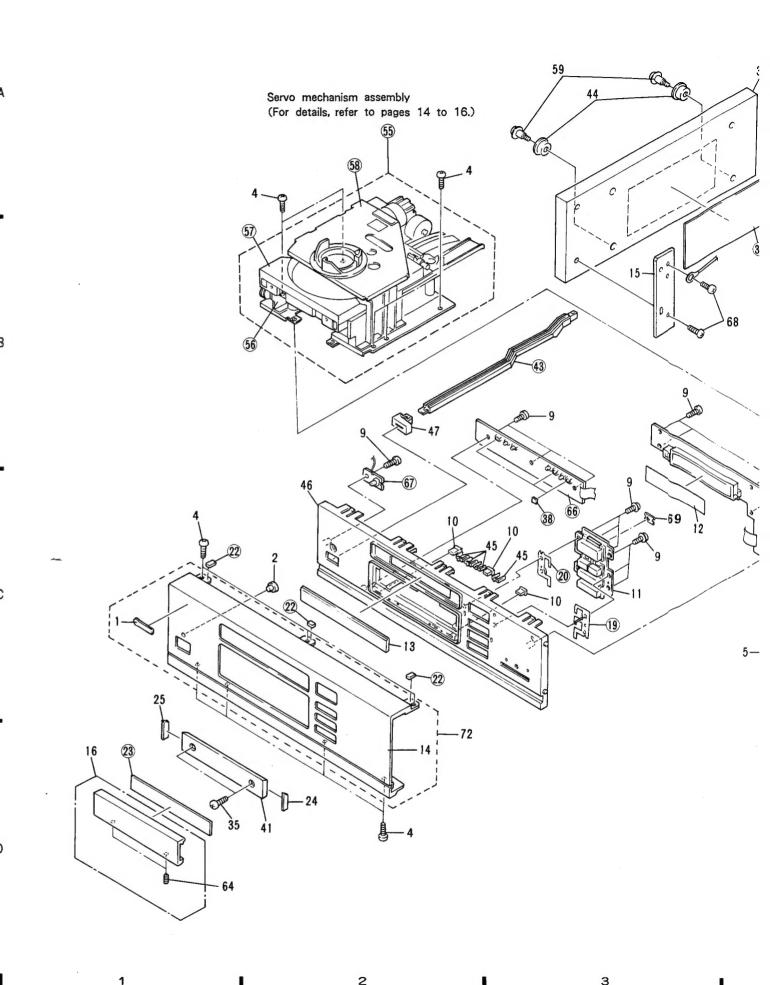


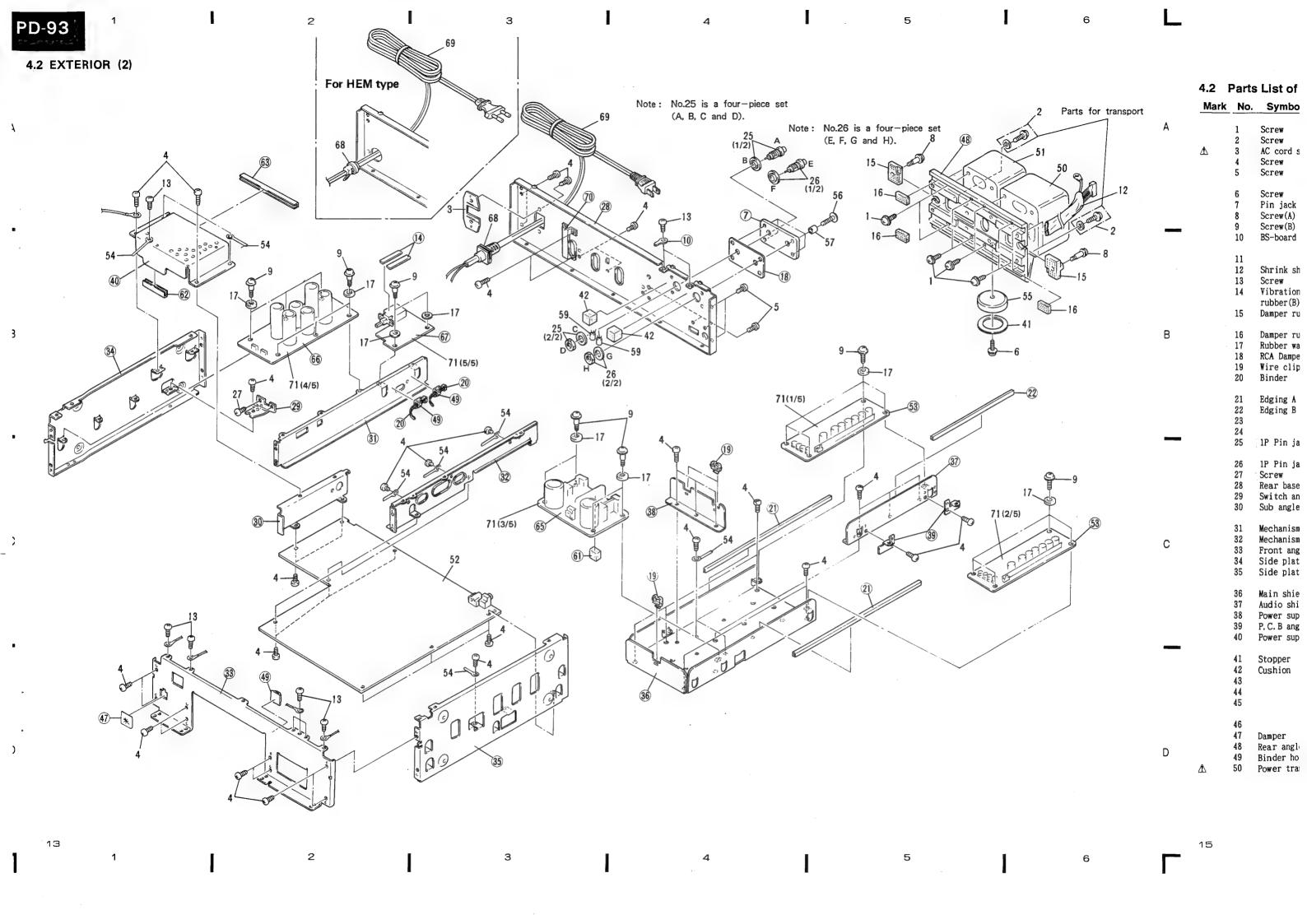






29 is partially put on on 28 with a both-sides tape to prevent from damage by the vibration in transport.



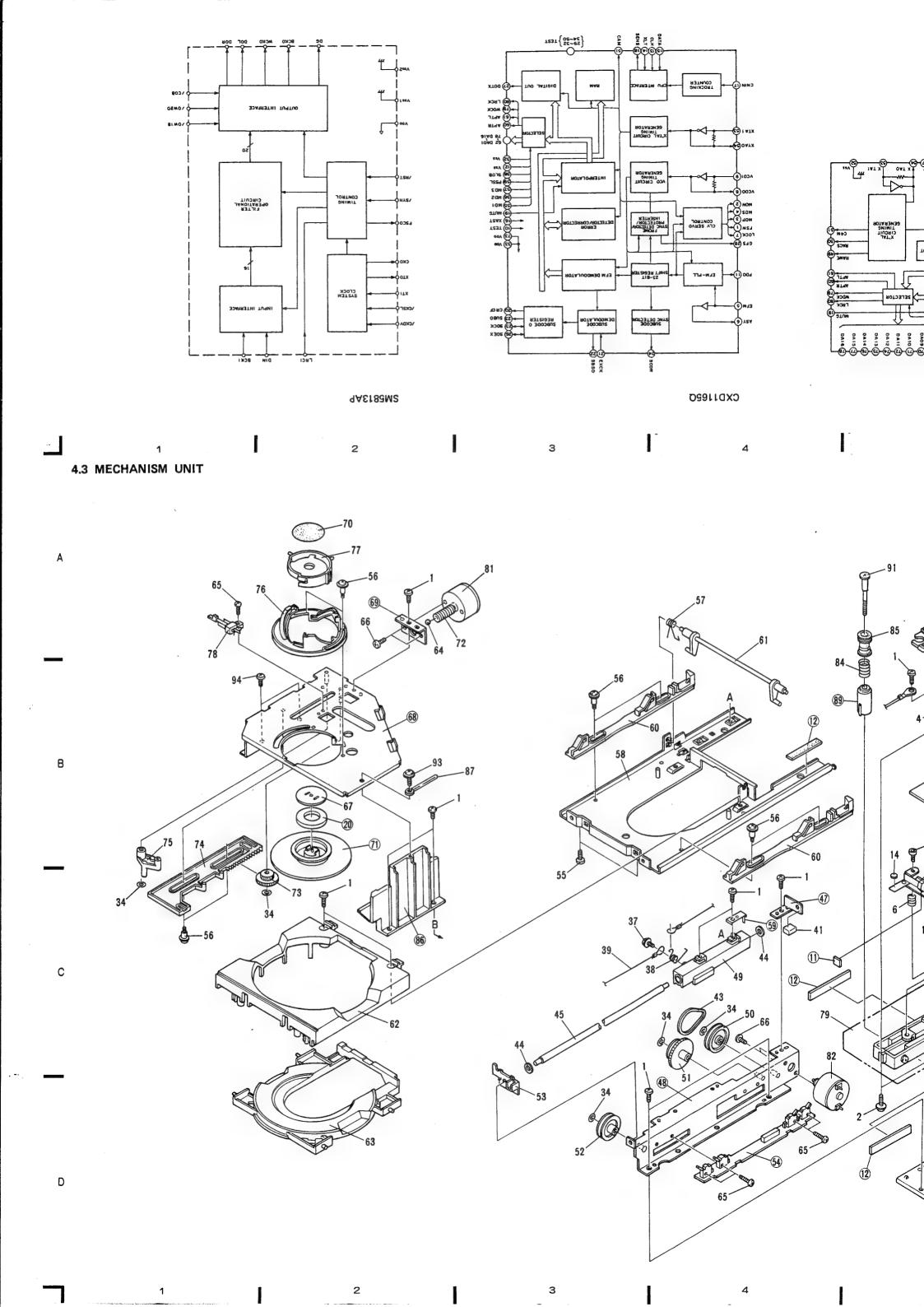


## 4.2 Parts List of Exterior(2)

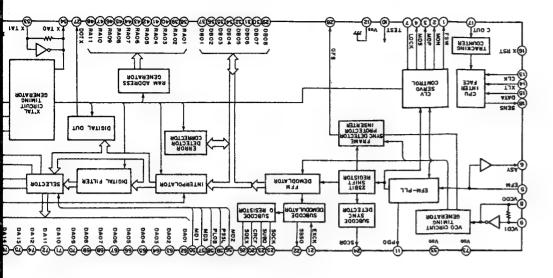
	Mark	No.	Symbol & Description	Part No.	Mark	No.	Symbol & Description	Part No.
Α		1 2	Screw Screw	AMZ40P080FMC AMZ40P180FRD	$\Delta$	51	Power transformer S/24VA	PTT1158 (KU/CA) PTT1157 (HEM)
	$\triangle$	3	AC cord spacer	ANG1153(KU/CA type)	$\odot \mathbb{A}$	52	Main board assembly	PWM1285
	حت	4	Screw	BBZ30P060FCC	O <sub>II</sub>	53	Audio monaural board	1 481203
		5	Screw	BBZ30P080FCC		50	assembly	
		J	SCIEW	BBZ30F080FCC		F.4		DANK 104
		•	•	*D500D100D00		54	Cord clamper	RNH-184
		6	Screw	IBZ30P120FCC		55	Insulator	VLL1038
		7	Pin jack name plate					
		8	Screw(A)	PBA1008		56	Screw	Z39-012
		9	Screw(B)	PBA1014		57	Bush	Z39-013
		10	BS-board lug			58		• • • • •
			_		<b>A</b>	59	Capacitor	CQSF101J50
		11				60	00,000	
,		12	Shrink shield 450L	PDM1003		00		••••
		13	Screw	PDZ30P060FCC		C1	Dubban ansara	
				FDZ30F000FCC		61	Rubber spacer	
		14	Vibration isolating			62	Edging C	
			rubber(B)			63	Edging D	
		15	Damper rubber(A)	PEB1054		64		• • • • •
						65	Power supply board (A)	
В		16	Damper rubber(B)	PEB1055			assembly	
		17	Rubber washer	PEB1136				
		18	RCA Damper rubber			66	Power supply board (S)	
		19	Wire clip			00	assembly	
		20	Binder			67	•	
		20	billder			67	Primary board assembly	an acc (m) (a)
		••				68	Strain relief	CM-22C(KU/CA type)
		21	Edging A					CM-22B(HEM type)
		22	Edging B		Æ	69	AC power cord	VDG1042(KU/CA type)
		23						PDG1003(HEM type)
		24				70	S cover	
		25	IP Pin jack L	PKB1012				
					$\odot \mathbb{A}$	71	Audio board assembly	PWM1286
		26	1P Pin jack R	PKB1013			Madio board assembly	1 ##1200
		27	Screw	PMZ30P060FCC				
		28	Rear base	rm230r000rcc				
		29	Switch angle					
		30	Sub angle					
		31	Mechanism angle L					
С		32	Mechanism angle R					
0		33	Front angle					
		34	Side plate L					
		35	Side plate R					
		00	orde prace n					
		36	Main shield plate					
		37	Audio shield plate					
		38	Power supply shield plate					
		39	P.C.B angle					
		40	Power supply cover					
		41	Stopper	PNM-051				
		42	Cushion	PNM1008				
		43						
		44		*****				
				• • • • •				
		45		••••				
		46						
		47	Damper					
_		48	Rear angle					
D		49	Binder holder					
	$\Delta$	50	Power transformer A/13VA	PTT1156 (KU/CA)				
	ىب	50	TOTAL ELGISTOLING IN/ 101A	PTT1155 (HEM)				

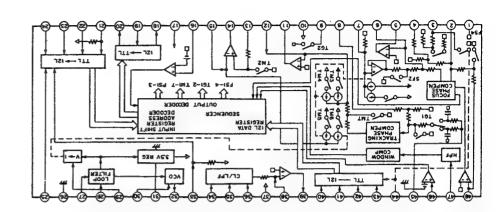
## 4.3 Parts List of Mechanism unit

No.	Symbol & Description	Part No.	Mark	No.	Symbol & Description	Part No
1	Screw	BBZ30P060FCC		51	Drive pulley	PNW1212
	Screw	IBZ30P080FCC		52	Pulley	PNW1213
	Screw	PBA1020		53	L guide	
						PNW1214
	Screw	PBA1024		54	Loading board assembly	
5	Spring	PBH1027		55	Screw	BBZ30P080FCC
6	Spring	PBH1028		56	Screw	PBA-125
	Spring	PBH1029		57	Slide cam spring	PBH1026
	Spring	PBK1021		58	Slide base	
	Spring	PBK1022		59	Earth plate	
	Plastic rivet	PBM-015		60	Slide cam	PNW1217
11	Cushion rubber			61	Interlocking lever unit	PNW1218
					_	
	Vibration isolating			62	Tray	PNW1745
	rubber(B)			63	Disc plate	PNW1581
13	Stopper rubber	PEB1035		64	Steel ball $\phi 4$	PBP-001
14	Hold rubber	PEB1048		65	Screw	PMZ20P080FMC
15	Disc table	PLA1024				
				66	Screw	PMZ26P040FMC
16	Aligning ring	PLA1025		67	York	PNB1049
17	Guide bar	PLA1026		68	Clamp base	
	Shaft	PLA1027		69	Motor holder	
	Roller	PLM1001	•	70	Disc cushion	PNM1025
	Magnet	12012		10	Disc cushion	11411020
				71	Clamper	
21	Screw	PMZ26P030FCU		72	Worm	PNW1220
22	Screw	PMZ26P060FCU		73	Worm wheel	PNW1221
	Screw	PMZ30P080FCU		74	Clamp drive plate	PNW1222
24	501 011			75	Clutch	PNW1223
25		• • • • •		10	Clutch	1 14# 1225
20		• • • • • • • • • • • • • • • • • • • •		76	Clamp cam	PNW1224
26	Adjust lever	DNID 1 0 4 9			•	
	•	PNB1048		77	Clamp holder	PNW1225
	Linear flexible board			78	Reef switch	VSK-015
	Carriage			79	Drive unit	PYY1038
	Mechanism base unit			80	Speed sensor unit	PYY1039
30	Stopper	PNW1432		01	Notes combly	DW1007
91 1	Dialum assauble	DEV1004		81	Motor assembly	PYY1097
	Pickup assembly	PWY1004		82	Motor assembly	PYY-507
	Spindle motor	PXM1005		83	Float spring	PBH1030
33				84	Float spring(F)	PBH1097
	Washer Washer	WT25D047D025 WT40D065D025		85	Damper rubber	PEB1036
00 1	420101	#140000000000		86	Slide guide	
36 5	Screw	ZMD30H040FBT		87	•	DNU_194
					Cord clamper	RNH-184
	Screw	IBZ30P060FCC		88	Mechanism chassis	
	fire spring	PBH1025		89	Mechanism support	
	fire unit	PBL1001	,	90	Earth lead unit	
40.		••••		91	Screw	PBA1021
41 (	Cushion rubber			91	oct 6M	
42				93	Screw	TP720D100ECC
	0.1+	DED 1027				IBZ30P100FCC
	Belt	PEB1037		94	Screw	PDZ30P060FCC
	Stopper	PEB1076				
45 (	Guide bar	PLA1028				
46						
	Holder					
	Loading base					
	Slider unit	DN#1210				
дч \	orraer, mirr	PNW1210				
	Gear pulley	PNW1211				









CXD11320Z

В

CXA1082AS

5 6

## 5. LINE VOLTAGE SELECTION

- Line voltage can be changed with the following steps.
  - 1. Disconnect the AC power cord.
  - 2. Remove the top cover.
  - 3. Change the transformer S (24VA) wire of terminal CN ♠ ① and CN ♠ ② as follows.

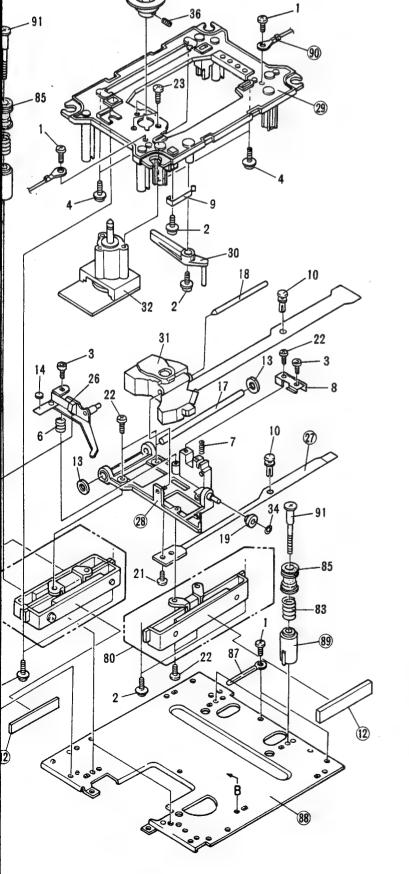
	Voltage	Terminal No. CN ⊗ - ①	Terminal No. CN (A) - (2)
_	220V	BLUE	PURPLE
	240V	PURPLE	BLUE

Change the transformer A (13VA) wire of terminal  $CN \otimes - \bigcirc$  and  $CN \otimes - \bigcirc$  as follows.

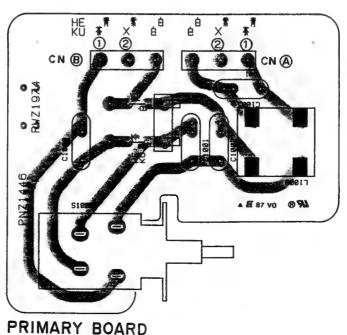
Voltage	Terminal No. CN® - ①	Terminal No. CN® - 2
220V	BLUE	PURPLE
240V	PURPLE	BLUE

4. Stick the line voltage label on the rear panel.

Part No.	Description
AAX-193	220V label
AAX-192	240V label



5

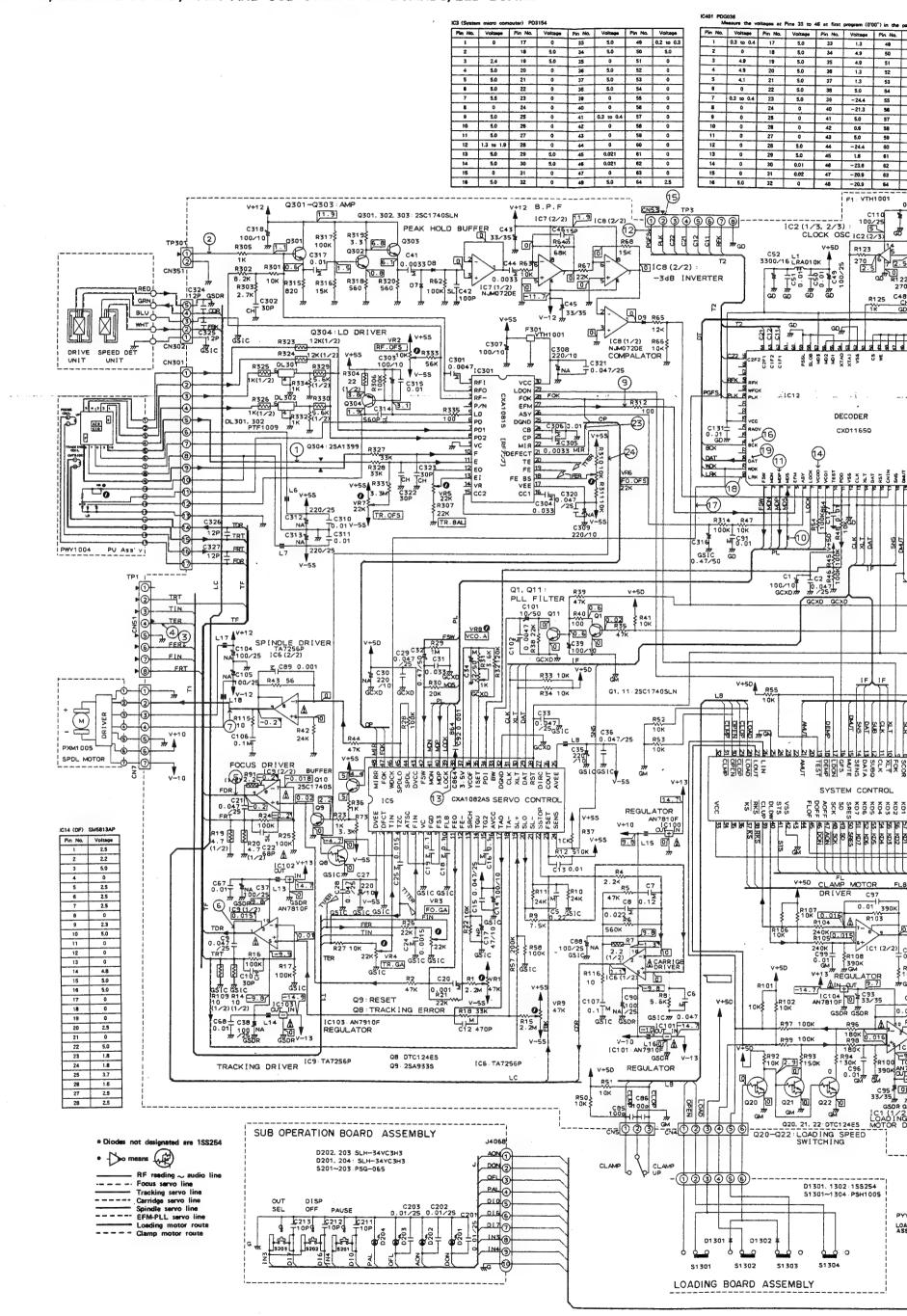


PRIMARY BOARD ASSEMBLY В

D

## SCHEMATIC AND P.C. BOARDS CONNECTION DIAGRAMS

6.1 SCHEMATIC DIAGRAMS AND P.C. BOARDS OF RESPECTIVE ASSEMBLIES FOR MAIN BOARD, LOADING BOARD, MAIN AND SUB OPERATION BOARDS, LED BOARD

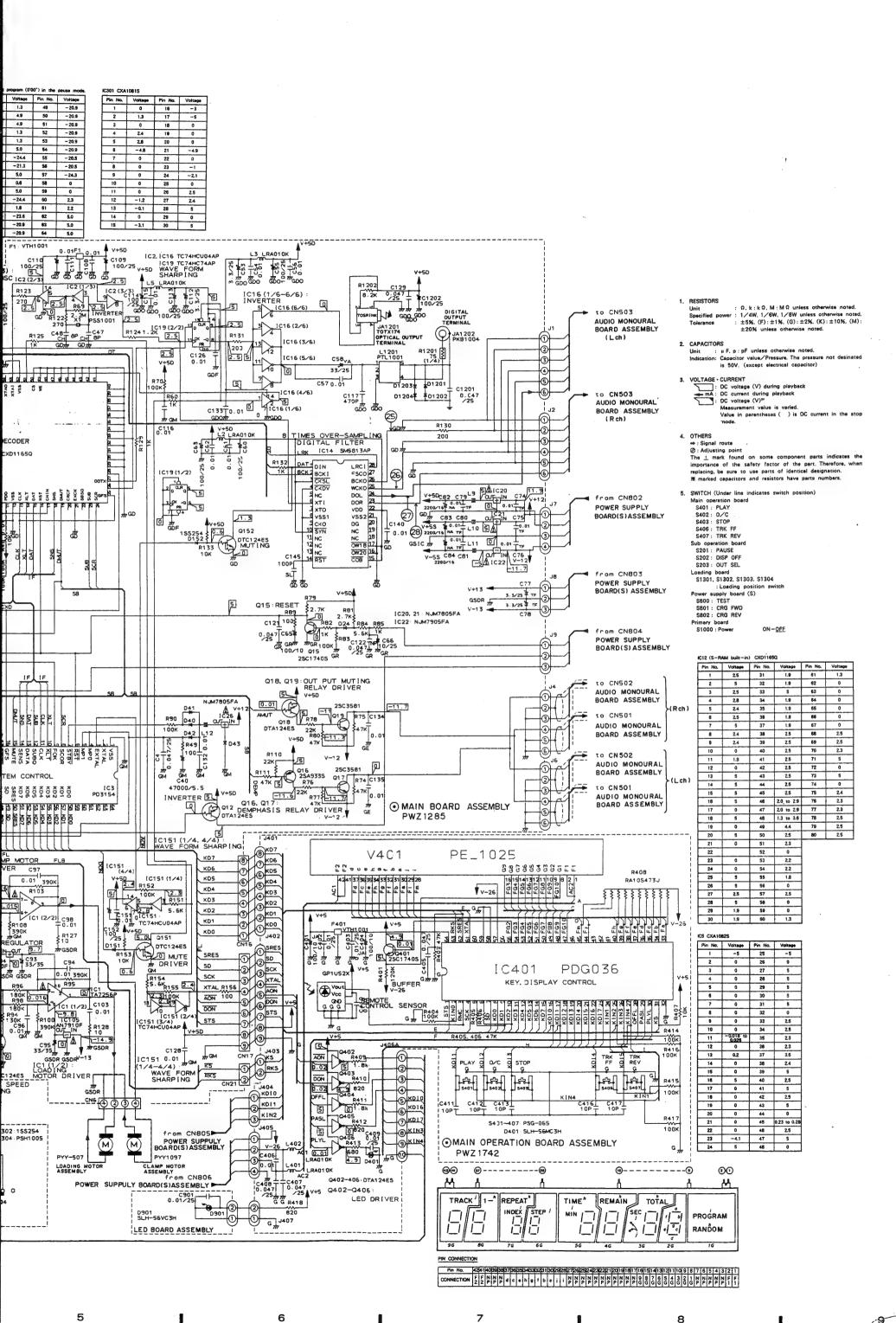


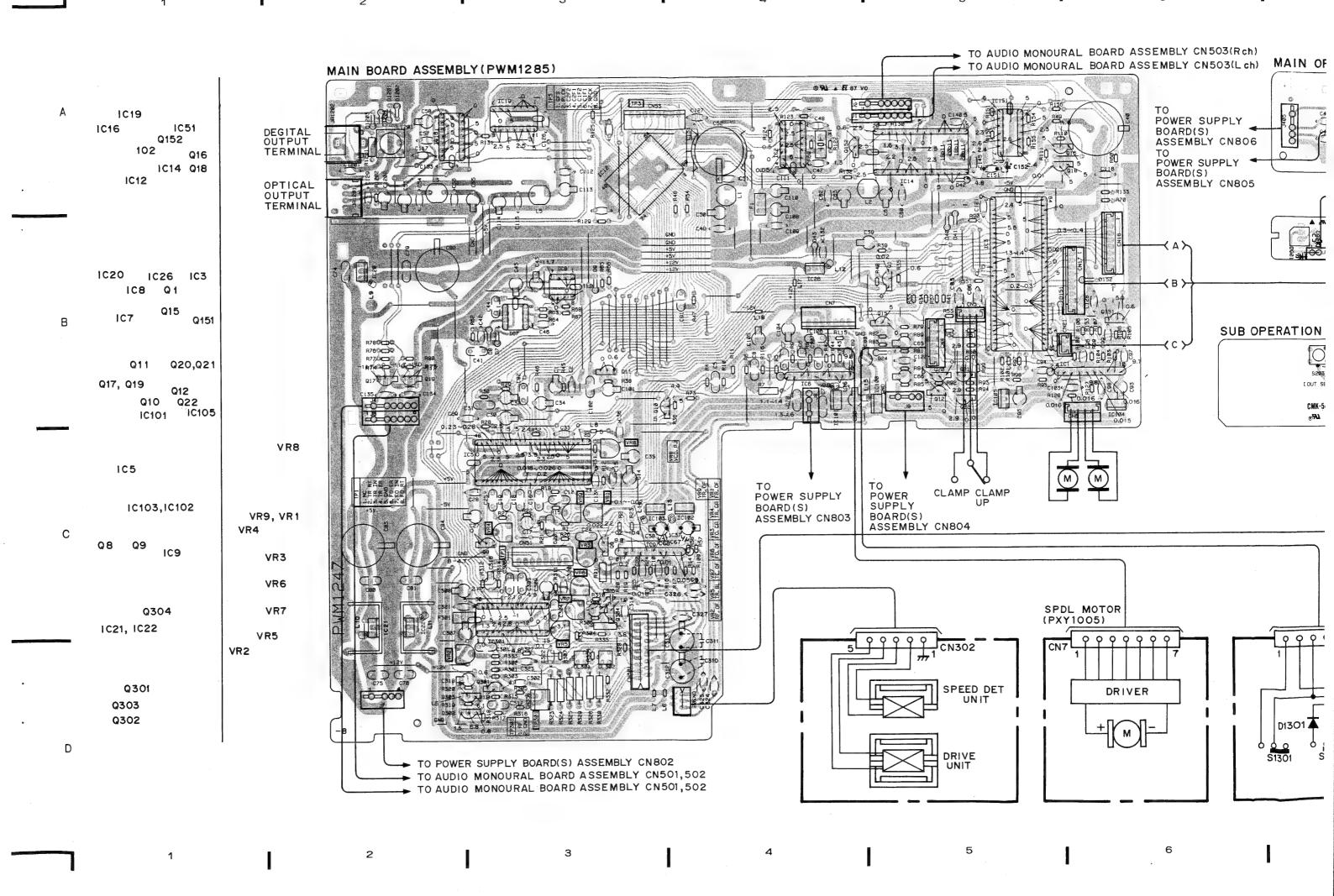
3

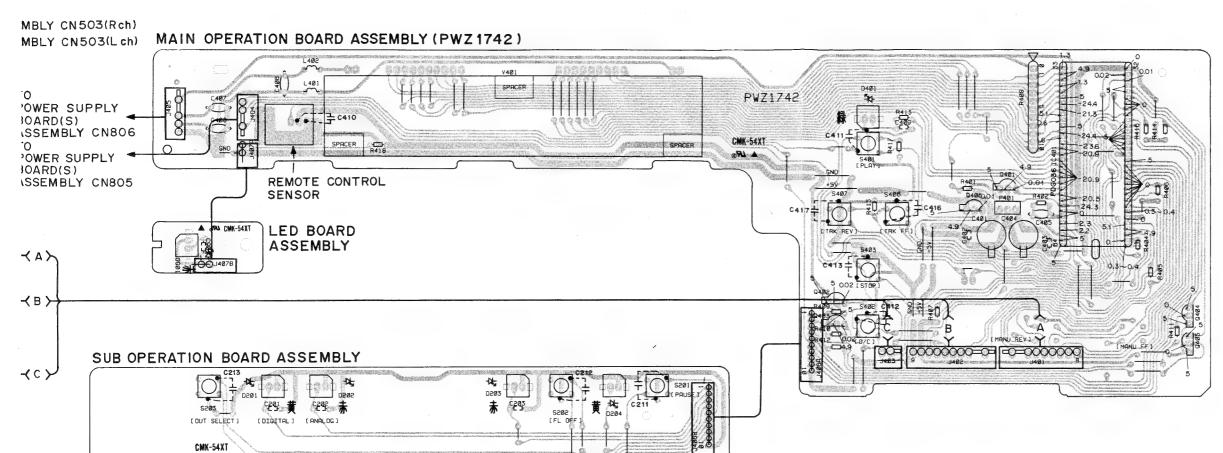
20

F

2







Pin No.	Voltage	Pin No.	Voltage	Pin No.	Voltage
1	2.5	31	1.9	61	1.3
2	5	32	1.9	62	0
3	2.5	33	5	63	0
4	2.8	34	1.9	64	0
5	2.4	35	1.9	65	0
6	2.5	36	1.9	66	0
7	5	37	1.9	67	0
8 .	2.4	38	2.5	68	2.5
9	2.4	39	2.5	69	2.5
10	0	40	2.5	70	2.3
11	1.8	41	2.5	71	5
12	0	42	2.5	72	0
13	5	43	2.5	73	5
14	5	44	2.5	74	0
15	5	45	2.5	75	2.4
16	5	46	2.0 to 2.9	76	2.3
17	0	47	2.0 to 2.9	77	2.3
18	5	48	1.3 to 3.6	78	2.5
19	0	49	4.4	79	2.5
20	5	50	2.5	80	2.5
21	0	51	2.3		
22		52	0		
23	0	53	2.2		
24	0	54	2.2		
25	5	55	1.8		
26	5	56	0		
27	2.5	57	2.5		
28	5	58	0 -		
- 20	1.9	59	0		

IC12 (S-RAM built-in) CXD1165Q

L801) are inser	rted to Pin of each	semiconductor.
P.C.B. pattern diagram indication	Corresponding part symbol	Part name
	E O O E O	Transistor
D S G		FET
014		:
<u></u>	<b>⊸</b>	Diode
æ_	<b>⊶</b> ✓	Zenner diode
74	~~~	LED
	<b>○</b>	Varactor
	•	Tact switch
	٠	Inductor
0	۰۳۰۰	Coil
3 2		Transformer
		Filter
ζ_,		Ceramic capacitor
$\subset \supset$		Mylar capacitor
* §( )		Styrol capacitor
g Z	<b>○──────</b> ○	Electrolytic capacitor (Non polarized)
□ F		Electrolytic capacitor (Noiseless)
€	<u>0−−</u> ₩+−−0	Electrolytic capacitor (Polarized)
		Electrolytic capacitor (Polarized)
	9	capacitor
	o-w-o	Semi-fixed resistor
		Resistor array
		Resistor
~	°	ricastor
	~	Resonator

Note: (L9 to L16, L504, L700, L701, L800 and

· ~

1. This P.C.B. connection diagram is viewed from the parts mounted side.
2. The parts which have been mounted on the board can be replaced with those shown with the corresponding wiring symbols listed in the above Table.
3. The capacitor terminal marked with \_\_\_\_ shows negative terminal.
4. The diode marked with O shows cathode side.
5. The transistor terminal marked with \_\_\_\_ shows emitter.

H0F

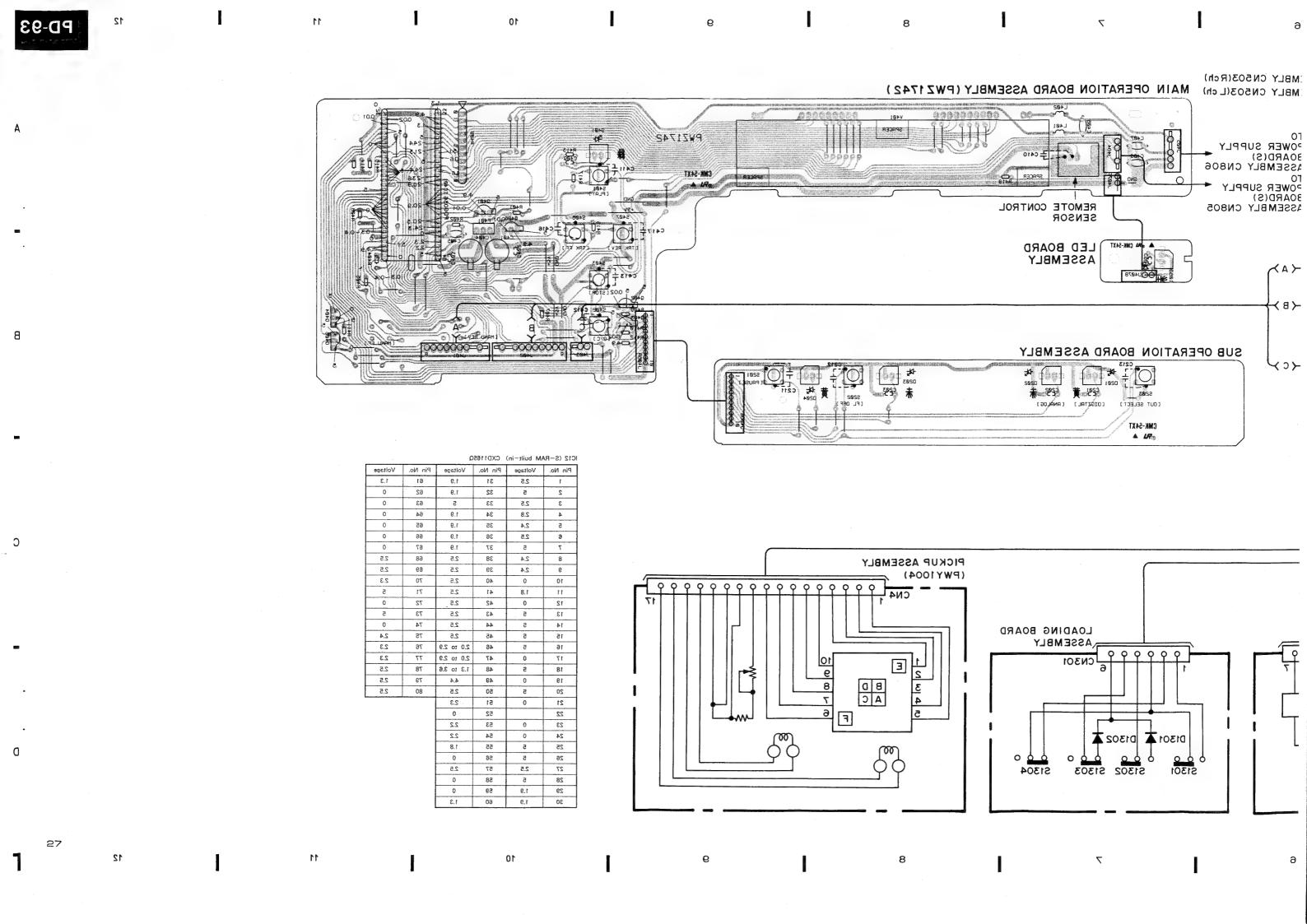
7	LOADING BOARD ASSEMBLY CN301  D1301 D1302  D1302 S1301 S1302 S1303 S1304	PICKUP ASSEMBLY (PWY1004)  CN4 0 0  CN4 1  1 E  3 A C  5

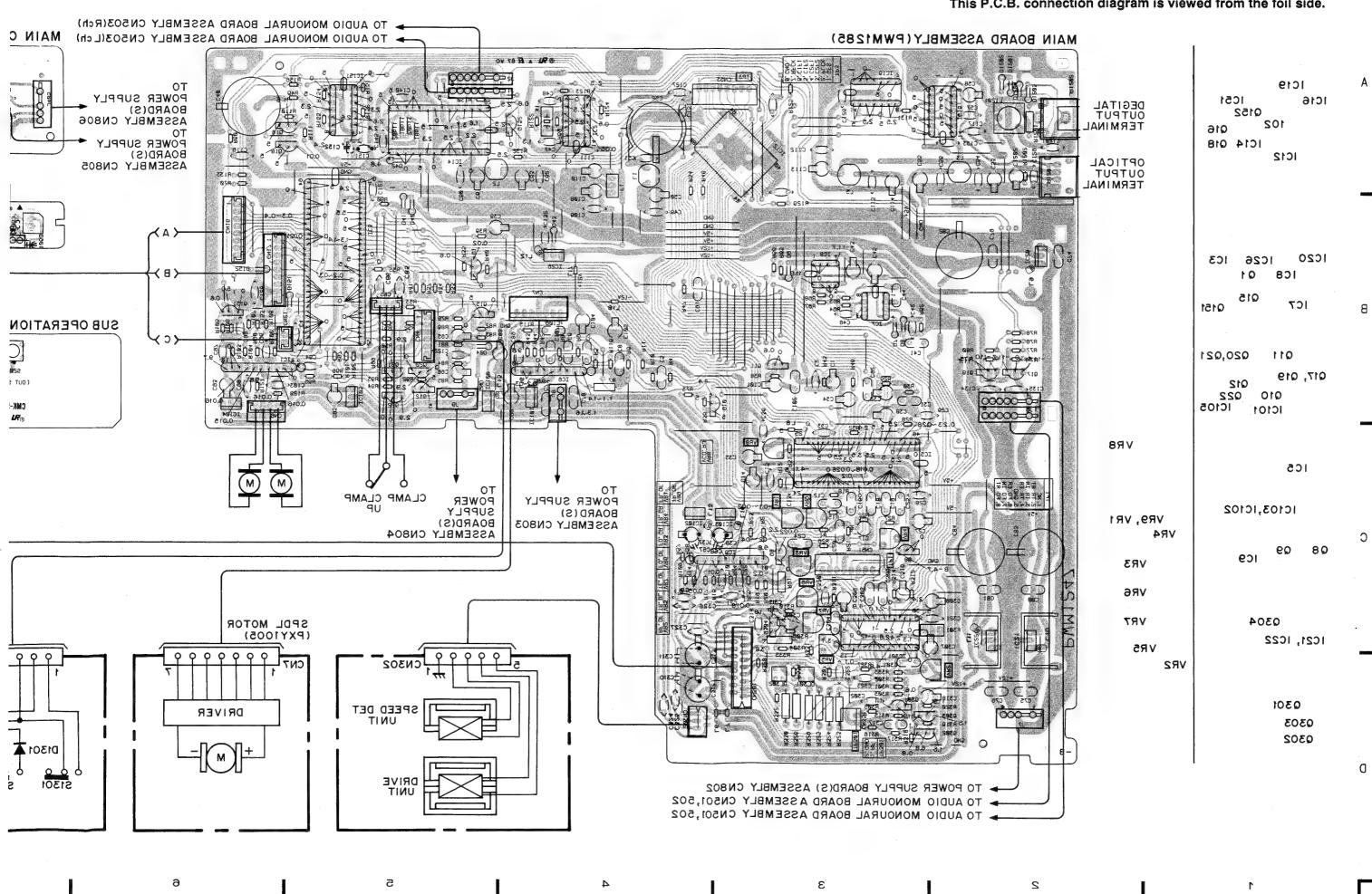
**▲** 1/P<sub>®</sub>

(PWY100	CN4	17
	E 10 9 8 A C 7 6 F 6	

8

30 1.9 60 1.3





MAIN OPERATION BOARD ASSEMBLY PWZ1973 PNZ1445 POWER
TRANSFORMAR(S) PUR (HEM Only) PRP BLK BRN(KU/CA) BLU(HEM) RED GRY BLK ВРИ RED GRY BLU PRP BRN GRY WHT(KU/CA, HEM) POWER
TRANSFORMAR(A) TO MAIN BOARD ASSEMBLY J9 TO MAIN BOARD ASSEMBLY J8 TO MAIN BOARD ASSEMBLY J 7 (WHT(KU/CA,HEM) RED BRN(KU/CA) BLU(HEM) BLK GRY PUR (HEM Only) BRW POWER SUPPLY BOARD(A) **ASSEMBLY** PWZ1972 PNZ1444  $\triangle$ PRIMARY BOARD ASSEMBLY AC POWER CORD AC 120 V (KU/CA) 220V (HEM) 50/60Hz 10

Α

8

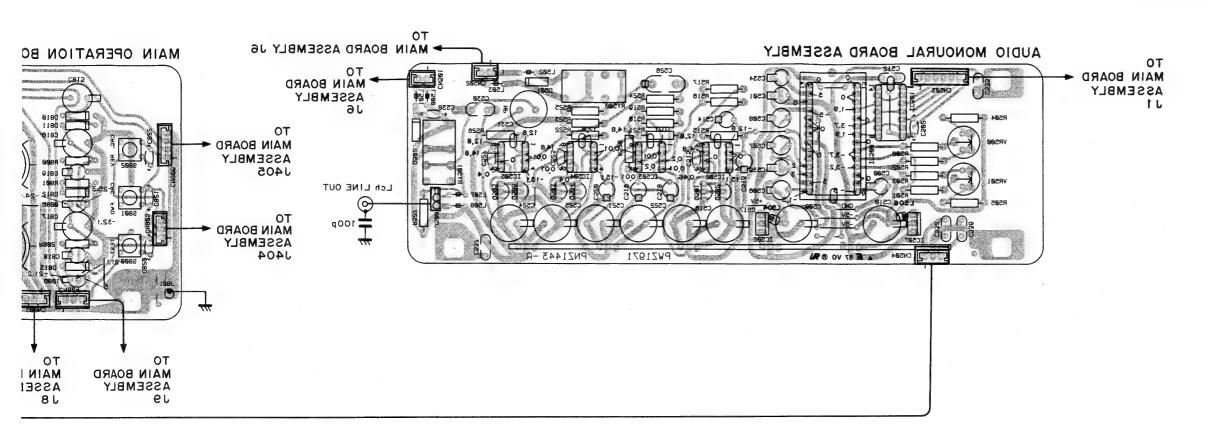
0

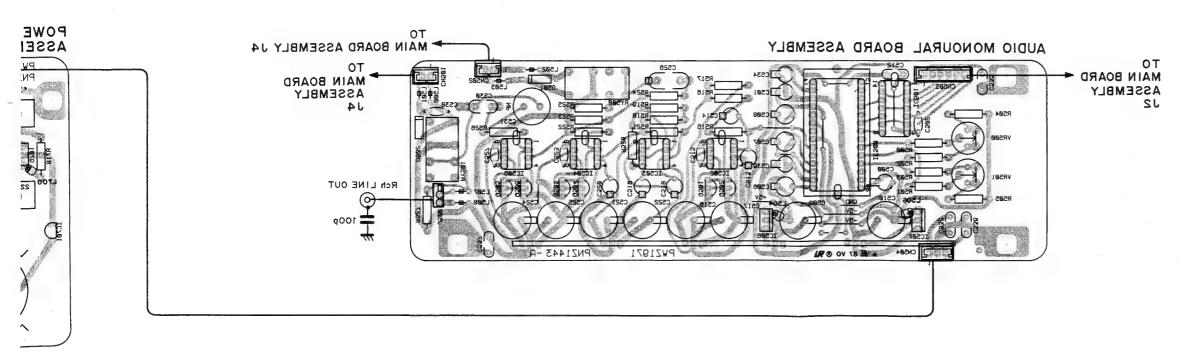
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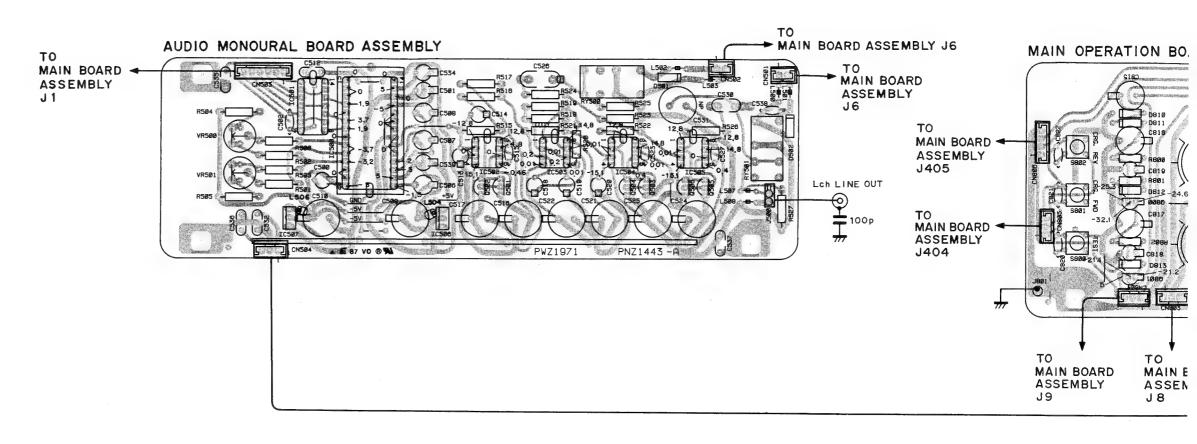
12

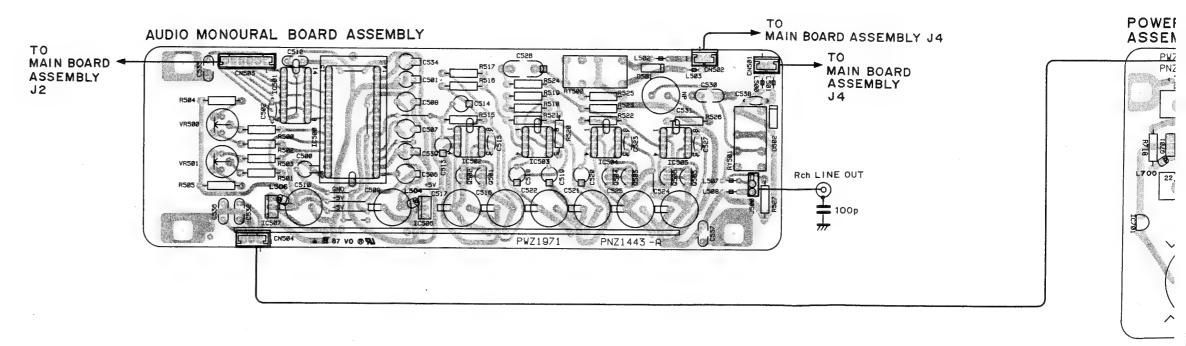
6.2 SCHEMATIC DIAGRAMS AND P.C. BOARDS OF RESPECTIVE ASSEMBLIES FOR AUDIO MONAURAL BOARDS (Lch), (Rch), POWER SUPPLY BOARDS (A), (S) AND PRIMARY BOARD

This P.C.B. connection diagram is viewed from the foil side.







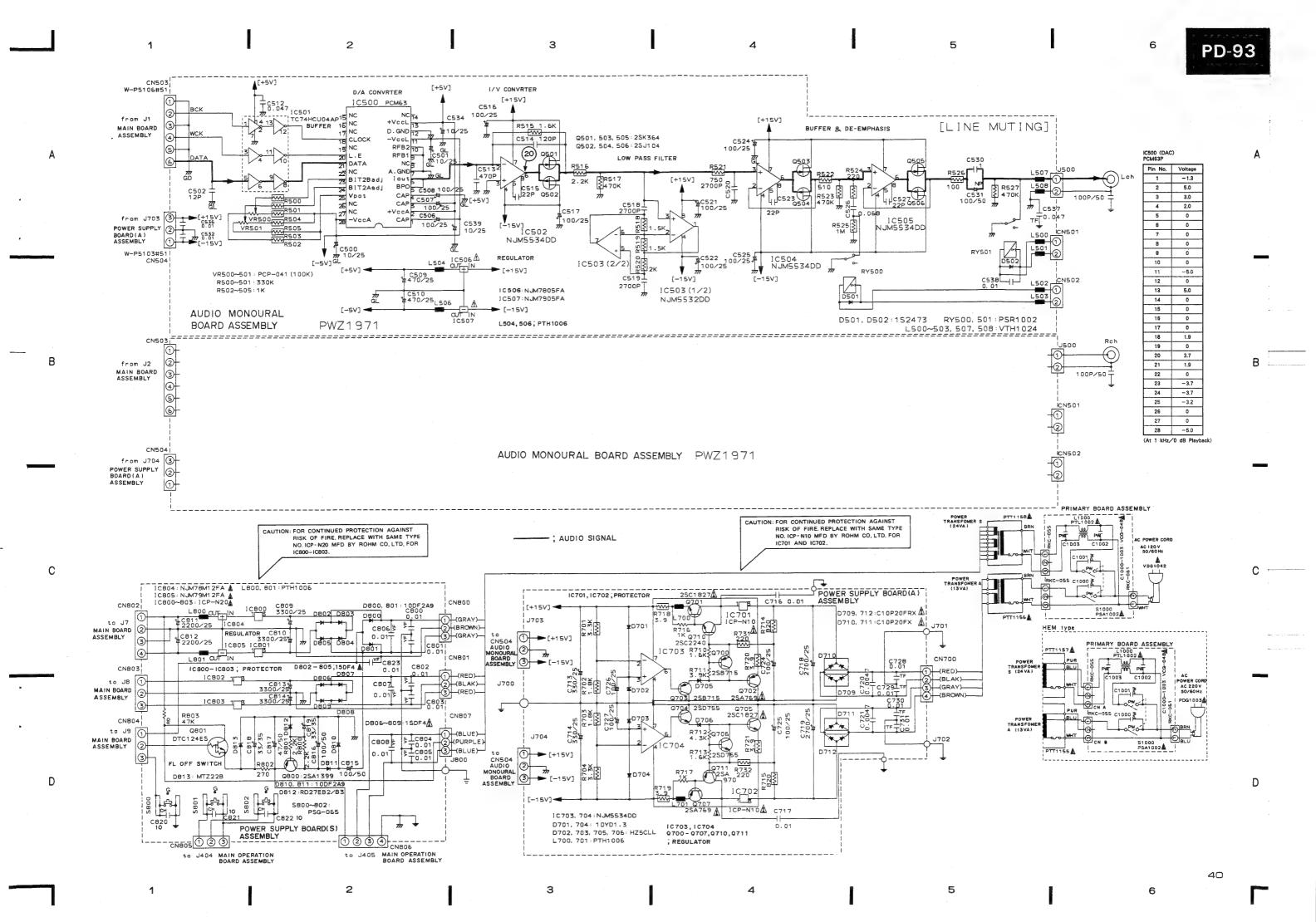


Α

В

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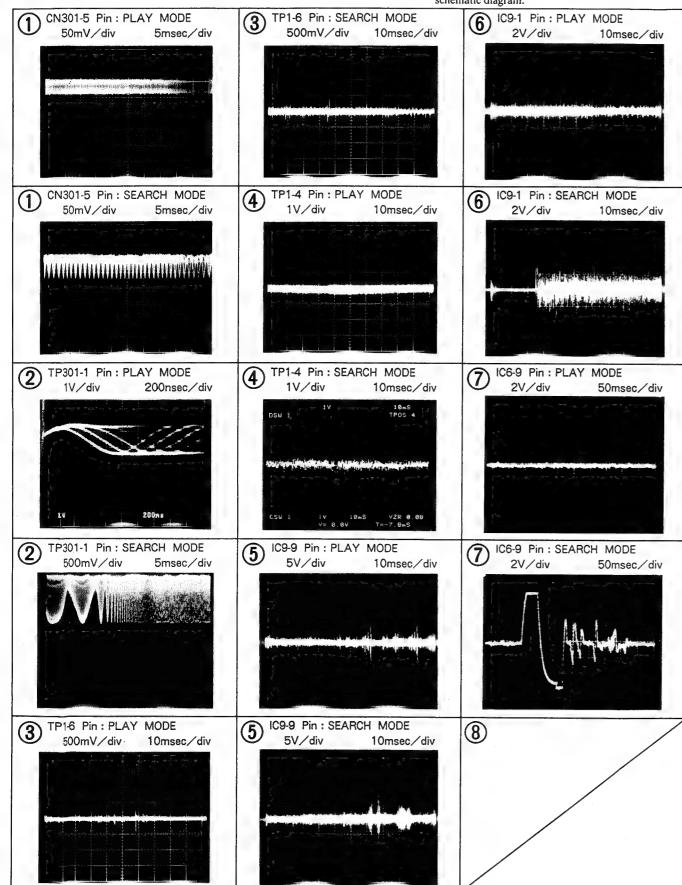
12

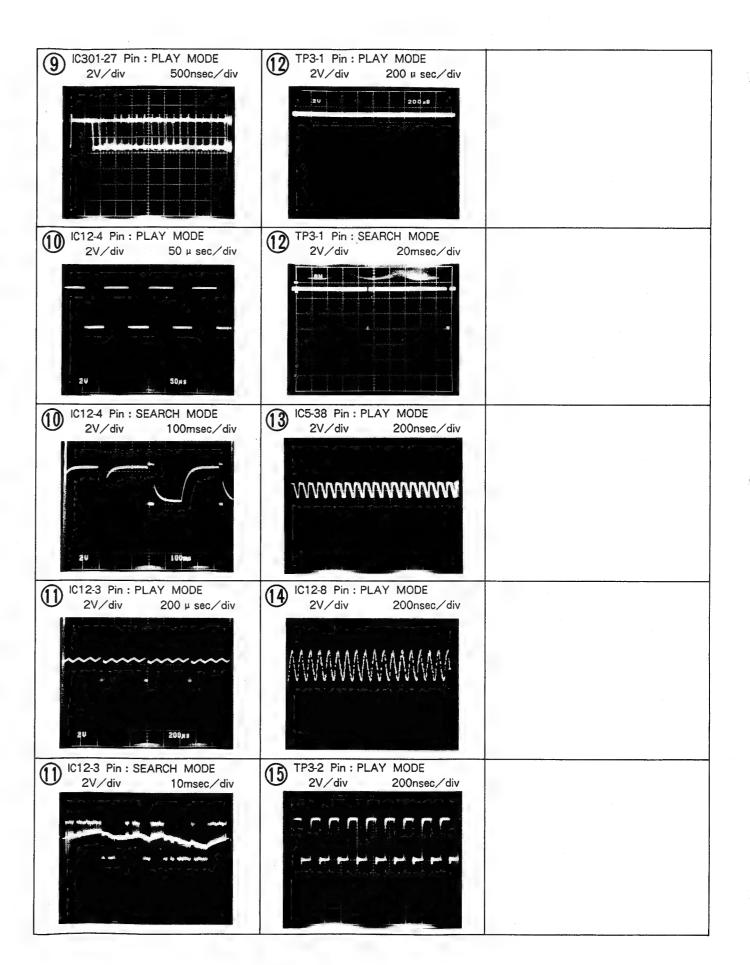


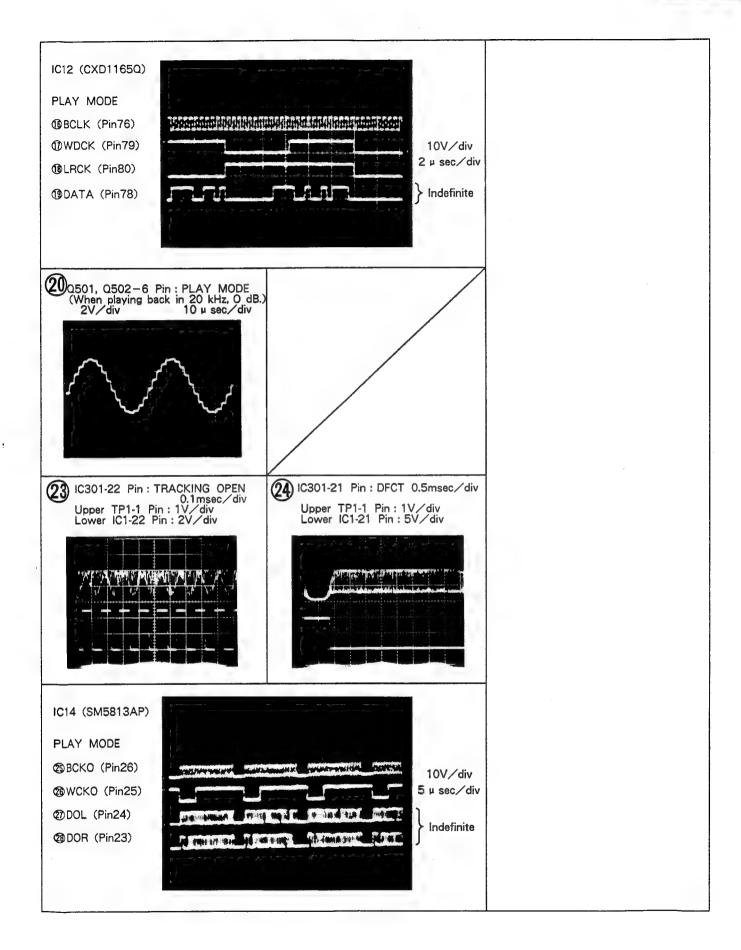


### **Wave Forms**

NOTE: The encircled numbers denote measuring points in the schematic diagram.









## 7. P.C.B's PARTS LIST

### NOTES:

- Parts without part number cannot be supplied.
- Parts marked by "O" are not always kept in stock. Their delivery time may be longer than usual or they may be unavailable.
- The  $\triangle$  mark found on some component parts indicates the impotance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- When ordering resistors, first convert resistance values into code form as shown in the following examples.

Ex.1 When there are 2 effective digits (any digit apart from 0), such as 560 ohm and 47k ohm (tolerance is shown by J=5%, and K=10%)

560 Ω	$\rightarrow 56 \times 10^{1} \rightarrow 561$	 RD1/4PS 5 6 1 J
0.5 Ω	→ 0R5 · · · · · · · · · ·	 RN2H 0 R 5 K
1.0	→ 010 · · · · · · · · · ·	 PSIPMIIMK

Ex.2 When there are 3 effective digits (such as in high precision metal film resistors).

 $5.62k \Omega \rightarrow 562 \times 10^1 \rightarrow 5621 \cdots RN1/4SR 5 6 2 1 F$ 

Mark No. Symbol & Description	n Part No.	Mark No. Symbol & Descript	ion Part No.
<b>● MAIN OPERATION BOA</b>	RD ASSEMBLY	SWITCHES	
(PWZ1742)		S201-203 SWITCH	PSG-065
SEMICONDUCTORS		CAPACITORS	
IC401 MICROCOMPUTER	PDG036	C201-203	CKPUYF103Z25
Q401 TRANSISTOR	2SC1740S	C211-213	CCPUCH100J50
Q402-406 TRANSISTOR	DTA124ES	. == ==	
D401	SLH-56MC3H	LED BOARD ASSEMBLY	
WITCHES		SEMICONDUCTORS	
S401-403 SWITCH	PSG-065	D901	SLH-56VC3H
S406, 407 SWITCH	PSG-065		
		CAPACITORS	
OILS/TRANSFORMERS		C901	CKPUYF103Z25
L401, 402	LRA010K		
F401 FILTER	VTH1001	AUDIO MONAURAL BOA	ARD ASSEMBL
APACITORS		SEMICONDUCTORS	
C401 ELECTR. CAPACITOR	CEJA101M10	IC500 D/A CONVERTER, IC	PCM63P-K
C402, 403	CKPUYF103Z25	IC501	TC74HCU04AP
C404 ELECTR, CAPACITOR	CEJA101M10	IC502	NJM5534DD
C405 CERAMIC CAPACITOR	CGCYF473Z25	IC503	NJM5532DD
C406 CERAMIC CAPACITOR	CKCYF103Z50	IC504, 505	NJM5534DD
C407,408 CERAMIC CAPACITOR	CGCYF473Z25	<b>⚠</b> IC506	NJM7805FA
C409. 410	CKPUYF103Z25	<b>⚠</b> IC507	NJM7905FA
C411-413	CCPUCH100J50	Q501	2SK364
C416. 417	CCPUCH100J50	Q502	2SJ104
	cci belliosso	Q503	2SK364
ESISTORS	DD1 /chwllillit	0504	00.1104
R401, 402 CARBONFILM RESISTOR	RD1/6PM DDJ	Q504	2SJ104
R404-407 CARBONFILM RESISTOR	RD1/6PM	Q505	2SK364
R408 RESISTOR ARRAY (47K)	RA10S	Q506	2SJ104
R409-418 CARBONFILM RESISTOR	RD1/6PM□□□J	D501, 502 DIODE	1S2473
THERS		RELAYS	
INFRARED RAYS RECEIVER	GP1U52X	RY500, 501 RELAY	PSR1002
V401 FLUORESCENT TUBE	PEL1025		
UB OPERATION BOARD	ASSEMBLY	COILS/TRANSFORMERS L500-503 FERRITE BEAD	VTH1024
		L504	PTH1006
EMICONDUCTORS		L504 L506	
D201	SLH-34YC3H3		PTH1006
		L507, 508 FERRITE BEAD	VTH1024
D202, 203	SLH-34VC3H3		
D204	SLH-34YC3H3		

rk No. Symbol & Description	n Part No.	widi	k No.	Symbol & Description	Part No.
PACITORS		COII	S/TRAN	SFORMERS	
C500, 501 (10/25)	PCH1063		L700, 70		PTH1006
C502 CERAMIC CAPACITOR	CCCCH120J50		2100,10	•	
		CAD	ACITOR	•	
C506-508 ELECTR. CAPACITOR	CENA101M25	CAP		_	CENTTOTHOL
C509, 510 ELECTR. CAPACITOR	CENA471M25			4 ELECTR. CAPACITOR	CENA331M25
C512 AUDIO FILM CAPACITOR	CFTXA473J50			7 CERAMIC CAPACITOR	CKCYF103Z50
			C718, 71	9 (2700/25)	PCH1064
C513	PCL1008		C720, 72	21	PCL1006
C514	PCL1007			7 ELECTR. CAPACITOR	CENA101M25
	CMA220J500		0101 11	DEBOTIC ON NOTION	0517110111100
C515 MICA CAPACITOR	_		C700 7	ALIDIO PILM CADACITOD	CETYALOSIES
C516, 517 (100/25)	PCH1084		C128-13	B1 AUDIO FILM CAPACITOR	CFTXA103J50
C518-520	PCL1026				
		RES	ISTORS		
C521, 522 (100/25)	PCH1084		R701-70		$RDM1/2P \square \square \square J$
C523 MICA CAPACITOR	CMA220J500		R710-7	3 CARBONFILM RESISTOR	RDR1/4PM
C524, 525 (100/25)	PCH1084		R714, 73	15	RDM1/2P□□□J
C526	PCL1009		R716, 7	7 CARBONFILM RESISTOR	RDR1/4PM
C527 MICA CAPACITOR	CMA220J500		R718-7		RDM1/2P
C327 BITCH CHI HOTTON	CHAIDEGGGGG			•	
C530	PCL1025		R731, 7	32	RDM1/2P□□□J
C531 (100/50)	PCH1088	D0	WP	HIDDLY BOARS	C) ACCELE
C532 AUDIO FILM CAPACITOR	CFTXA103J50	40	WEH ?	SUPPLY BOARD (	o) assembl
C534 (10/25)	PCH1063			•	
C536 AUDIO FILM CAPACITOR	CFTXA103J50	SEM	ICONDU	CTORS	
		$\Delta$		303 IC PROTECTOR	ICP-N20
C537 AUDIO FILM CAPACITOR	CFTXA473J50	$\Delta$	IC804		NJM78M12FA
C538 CERAMIC CAPACITOR	CKCYF103Z50		IC805		NJM79M12FA
		$\Delta$		ANGIOTOD	
C539 (10/25)	PCH1063			RANSISTOR	2SA1399
<b>T</b>			Q801 TI	RANSISTOR	DTC124ES
R500-505	RDM1/2P		D800, 80	01	10DF2FA9
R515, 516 CARBONFILM RESISTOR	RDM1/2P	$\Delta$	D802-8		15DF4
R517	RDM1/2P J		D810, 8		10DF2FA9
R518-522 CARBONFILM RESISTOR	RDM1/2P□□□F			ENER DIODE	RD27EB2
R523-527	RDM1/2P□□□J		D813		MTZ22B
YR500, 501	PCP-041	SWI	TCHES		
			S800-8	2 SWITCH	PSG-065
WER SUPPLY BOARD (	A) ASSEMBLY				
IICONDUCTORS		COI	LS/THAN L800, 80	ISFORMERS	PTH1006
	ICP-N10		2000, 0	,_	1 1111000
IC701, 702 IC PROTECTOR			AOITOR	6	
1C703, 704	NJM5534DD	CAP	ACITOR		***********
Q700 TRANSISTOR	2SB715			08 CERAMIC CAPACITOR	CKCYF103Z50
Q701 TRANSISTOR	2SC1827			10 (3300/25)	PCH1086
Q702 TRANSISTOR	2SA769		C811, 8	12 (2200/25)	PCH1087
			C813. 8	14 (3300/25)	PCH1086
Q703 TRANSISTOR	2SB715			16 ELECTR. CAPACITOR	CENA101M50
Q704 TRANSISTOR	2SD755		0010, 0		
•			C017 E	POTE CARACITOR	CENA470M50
Q705 TRANSISTOR	2SC1827			LECTR. CAPACITOR	
Q706 TRANSISTOR	2SD755			19 ELECTR. CAPACITOR	CEAS330M35
Q707 TRANSISTOR	2SA769			22 CERAMIC CAPACITOR	CCCCH100D50
OCIA MININGSONO	0000010		C823 C	ERAMIC CAPACITOR	CKCYF103Z50
Q710 TRANSISTOR	2SC2240	<b>1 1 1 1 1 1 1 1 1 1</b>	ICTASS	•	
Q711 TRANSISTOR	2SA970	RES	ISTORS		
D701 C. R. D.	10YD1. 3		R800-8	2 CARBONFILM RESISTOR	RDR1/4PM
D702, 703	HZ5CLL			ARBONFILM RESISTOR	RD1/4PM□□□J
D704 C. R. D.	10YD1. 3				
PIVE OF IN PE	2022. V	PR	MARY	BOARD ASSEME	BLY
D705, 706	HZ5CLL				
D709	C10P20FRX	SWI	TCHES		
D710, 711	C10P20FX	Δ	S1000		PSA1002
D710, 711 D712	C10P20FRX	41	21000		10/11000
0114	OTAL BALIN	COII	C/TDAN	SFORMERS	
					DOT 1000
		<b>⚠</b>	L1000 1	ILICA	PTL1002

Wate	No. Symbol & Description	Part No.	Mark No. Symbol & Description	Part No.
CAPA	ACITORS		L2, 3	LRA010K
f\	C1000-1003 CAPACITOR (CERAMIC)	VCG-048	L5	LRA010K
_			L6,7 FERRITE BEAD	VTH1024
THE	ERS		L8	VTH1013
	CAPACITOR COVER	REC-297	L9	PTH1006
M G	AIN BOARD ASSEMBLY	/(PWM1285)	F1 FILTER	VTH1001
		(1 171111200)	F301 FILTER	VTH1001
SEMI D	CONDUCTORS ·	TA7256P	CAPACITORS	
7	IC100	AN7810F	C1 ELECTR. CAPACITOR	CEAS101M10
		AN7910F	C10 CERAMIC CAPACITOR	CCCCH300J50
7	IC101	AN7810F	C101 ELECTR. CAPACITOR	CEAS100M50
7	IC102	AN7910F	C102 MYLOR FILM CAPACITOR	
7	IC103	VINIATOL	C103 CERAMIC CAPACITOR	CQMA472J50 CKCYF103Z50
7	IC104	AN7810F		
7	IC105	AN7910F	C104, 105 ELECTR. CAPACITOR	CENA101M25
	IC12 EFM DEMODULATION IC	CXD1165Q	C106, 107 MYLOR FILM CAPACITOR	CQMA104J50
	IC14 IC	SM5813AP	C108 CERAMIC CAPACITOR	CKCYF103Z50
	IC151	TC74HCU04AP	C109, 110 ELECTR. CAPACITOR	CENA101M25
			C111 CERAMIC CAPACITOR	CKCYF103Z50
	IC16	TC74HCU04AP		
	IC19	TC74HC74AP	C112 (3. 3/25)	PCH1083
	IC2	TC74HCU04AP	C113, 114 ELECTR. CAPACITOR	CENA101M25
7	IC20, 21	NJM7805FA	C115, 116 CERAMIC CAPACITOR	CKCYF103Z50
7	IC22	NJM7905FA	C117 CERAMIC CAPACITOR	CCCSL471J50
_			C12 MYLOR FILM CAPACITOR	CQMA471J50
7	IC26	NJM7805FA		Odmi1111200
2	IC3 MICROCOMPUTER, IC	PD3154	C1201 CERAMIC CAPACITOR	CGCYF473Z25
	IC301 PRE AMP IC	CXA1081S	C1202 ELECTR. CAPACITOR	CENA101M25
	IC5 SERVO CONTROL IC	CXA1082AS	C121, 122 CERAMIC CAPACITOR	CGCYF473Z25
7	IC6	TA7256P	C126-128 CERAMIC CAPACITOR	CKCYF103Z50
7	100	1112001	C129 CERAMIC CAPACITOR	CGCYF473Z25
	IC7, 8	NJM072DE		
7	IC9	TA7256P	C13 CERAMIC CAPACITOR	CKCYF103Z50
	Q1 TRANSISTOR	2SC1740SLN	C131-135 CERAMIC CAPACITOR	CKCYF103Z50
	Q11 TRANSISTOR	2SC1740SLN	C14 ELECTR. CAPACITOR	CEAS101M10
	Q12 TRANSISTOR	DTA124ES	C140 CERAMIC CAPACITOR	CKCYF103Z50
			C145 CERAMIC CAPACITOR	CCCSL101J50
	Q10, Q15 TRANSISTOR	2SC1740S		
	Q151, 152 TRANSISTOR	DTC124ES	C15 CERAMIC CAPACITOR	CGCYF473Z25
	Q16 TRANSISTOR	2SA933S	C151 CERAMIC CAPACITOR	CKCYF103Z50
	Q17 TRANSISTOR	2SC3581	C152 ELECTR. CAPACITOR	CENA101M25
	Q18 TRANSISTOR	DTA124ES	C16 MYLOR FILM CAPACITOR	COMA104J50
	•		C17 ELECTR. CAPACITOR	CEANP470M10
	Q19 TRANSISTOR	2SC3581		
	Q20-22 TRANSISTOR	DTC124ES	C18, 19 MYLOR FILM CAPACITOR	CQMA104J50
	Q301-303 TRANSISTOR	2SC1740SLN	C2 CERAMIC CAPACITOR	CGCYF473Z25
	Q304 TRANSISTOR	2SA1399	C20 MYLOR FILM CAPACITOR	CQMA102J50
	Q8 TRANSISTOR	DTC124ES	C21 CERAMIC CAPACITOR	CGCYF473Z25
		<del> </del>	C22 CERAMIC CAPACITOR	CCCSL680J50
	Q9 TRANSISTOR	2SA933S		
	D1201-1204 DIODE	1SS254	C24 MYLOR FILM CAPACITOR	CQMA152J50
	D151, 152 DIODE	1SS254	C25 MYLOR FILM CAPACITOR	CQMA153J50
	D24 DIODE	1SS254	C27 ELECTR. CAPACITOR	CENA221M10
	D40-43 DIODE	1SS254	C28, 29 CERANIC CAPACITOR	CGCYF473Z25
			C30 ELECTR. CAPACITOR	CENA221M10
	D7-9 DIODE	1SS254	C301 MYLOR FILM CAPACITOR	COMMATSIED
OII	S/TRANSFORMERS		C302 CERAMIC CAPACITOR	CQMA472J50 CCCCH300J50
	Ll	LRA010K	C303 ELECTR. CAPACITOR	CEAS101M10
	L10-12	PTH1006	C304 MYLOR FILM CAPACITOR	
		PTL1001		CQMA333J50
	L1201 COIL L13-16	PTH1006	C305 MYLOR FILM CAPACITOR	CQMA332J50
	6 13 18	LIUIOOO		
	L17, 18	VTH1017	C306 MYLOR FILM CAPACITOR	CQMA103J50

Mark	No.	Symbol & Description	Part No.	Mark	No.	Symbol & Description	Part No.
	C207 EII	ECTR. CAPACITOR	CEAS101M10		CSS FIF	CTR. CAPACITOR	CENA101M25
		9 ELECTR, CAPACITOR	CENA221M10			OR FILM CAPACITOR	CQMA102J50
		OR FILM CAPACITOR	CQMA333J50			MIC CAPACITOR	CGCYF473Z25
		1 CERAMIC CAPACITOR	CKDYF103Z50			CTR. CAPACITOR	CENA101M25
						OR FILM CAPACITOR	CQMA103J50
		3 ELECTR. CAPACITOR	CENA221M25		000 107	AD DILL CADACITOD	COMMISSION
	C314		CCCSL561J50			OR FILM CAPACITOR	CQMA102J50
		RAMIC CAPACITOR	CKDYF103Z50			CTR. CAPACITOR	CEAS330M35
		ECTR. CAPACITOR	CEASR47M50			AMIC CAPACITOR	CKCYF103Z50
		LOR FILM CAPACITOR	CQMA103J50			CTR. CAPACITOR CERAMIC CAPACITOR	CEAS330M35 CKCYF103Z50
		ECTR. CAPACITOR	CEAS101M10				
		CTR. CAPACITOR	CEASR47M50	RESIS			DD4 (4D4)
		1 CERAMIC CAPACITOR	CGCYF473Z25			ONFILM RESISTOR	RD1/6PM□□□J
		3 CERAMIC CAPACITOR	CCCCH300J50			BONFILM RESISTOR	RD1/6PM□□□J
	C324-32	7 CERAMIC CAPACITOR	CCCCH120J50			8 CARBONFILM RESISTOR	RD1/6PM UJ
					R109 CA	RBONFILM RESISTOR	RDR1/2PM□□□J
	C33 CER	AMIC CAPACITOR	CGCYF473Z25		R11 CAR	BONFILM RESISTOR	RD1/6PM□□□J
	C34 ELE	CTR. CAPACITOR	CEAS220M50				
	C35 ELE	CTR, CAPACITOR	CENA221M10		R110, 11	1 CARBONFILM RESISTOR	RD1/6PM□□□J
		AMIC CAPACITOR	CGCYF473Z25		R115, 11	6 CARBONFILM RESISTOR	RD1/6PM□□□J
	C37, 38	ELECTR, CAPACITOR	CENA101M25		R12 CAR	BONFILM RESISTOR	RD1/6PM□□□J
	001, 00				R1201 C	CARBONFILM RESISTOR	RDR1/4PM J
	C39 ELE	CTR. CAPACITOR	CEAS101M10			CARBONFILM RESISTOR	RD1/6PM□□□J
		MIC CAPACITOR	CGCYF473Z25				
		000/5.5)	PCH1062		R122-12	5 CARBONFILM RESISTOR	RD1/6PM J
	CA1 MYT	OR FILM CAPACITOR	CQMA332J50		R127-13	3 CARBONFILM RESISTOR	RD1/6PM UJ
		AMIC CAPACITOR	CCCSL101J50			BONFILM RESISTOR	RDR1/2PM J
	C4D CDM	mile om nerron	0000210100			BONFILM RESISTOR	RD1/6PM□□□J
	CAS FLE	CTR. CAPACITOR	CEAS330M35			6 CARBONFILM RESISTOR	RD1/6PM
		OR FILM CAPACITOR	CQMA332J50				
		CTR. CAPACITOR	CEAS330M35		R16-18	CARBONFILM RESISTOR	RD1/6PM□□□J
		AMIC CAPACITOR	CCCSL150J50			RESISTOR	RDR1/2PM□□□J
		CERAMIC CAPACITOR	CCCCH080D50			CONFILM RESISTOR	RD1/6PM
	041, 40	CERAMIC CAPACITOR	CCCCIOOODSO			RBONFILM RESISTOR	RDR1/2PM J
	CAO ELE	CTR, CAPACITOR	CENA101M25			CARBONFILM RESISTOR	RD1/6PM J
		R FILM CAPACITOR	COMA224J50		R21 00	CARDONI IDM RESISTOR	
		CERAMIC CAPACITOR	CKCYF103Z50		P201_20	3 CARBONFILM RESISTOR	RD1/6PM□□□J
			CEAS332M16			ARBONFILM RESISTOR	RDR1/2PM
		CTR. CAPACITOR	PCH1083			7 CARBONFILM RESISTOR	RD1/6PM J
	C53 (3.	3/25)	rCn1065			AL FILM RESISTOR	RN1/6PQ
	CE 4 CED	AMIC CAPACITOR	CKCYF103Z50			2 CARBONFILM RESISTOR	RD1/6PM
					W210-21	12 CARBONFILM RESISTOR	KD1/OLW[][]
		CTR. CAPACITOR	CENA101M25		D014 01	9 CARBONFILM RESISTOR	RD1/6PM□□□J
		AMIC CAPACITOR	CKCYF103Z50				
	• • • • • • • • • • • • • • • • • • • •	IO FILM CAPACITOR	CFTXA103J50			RBONFILM RESISTOR	RD1/6PM
	C58 ELE	CTR. CAPACITOR	CEYA330M25			ARBONFILM RESISTOR	RD1/6PM
						26 CARBONFILM RESISTOR	RDR1/2PM UJ
		R FILM CAPACITOR	CQMA473J50		R327, 32	28 CARBONFILM RESISTOR	RD1/6PM□□□J
		CTR. CAPACITOR	CENA101M25				
		CERAMIC CAPACITOR	CKCYF103Z50			ARBONFILM RESISTOR	RDR1/2PM
		CTR. CAPACITOR	CENA101M25			RBONFILM RESISTOR	RD1/6PM
	C65 ELE	CTR. CAPACITOR	CEAS101M10			ARBONFILM RESISTOR	RDR1/2PM□□□J
						35 CARBONFILM RESISTOR	RD1/6PM□□□J
	C66 ELE	CTR. CAPACITOR	CEANP100M25		R34-39	CARBONFILM RESISTOR	RD1/6PM□□□J
	C67, 68	CERAMIC CAPACITOR	CKDYF103Z50				
	C7 MYLC	OR FILM CAPACITOR	CQMA124J50		R4 CARE	BONFILM RESISTOR	RD1/6PM□□□J
	C74-76	AUDIO FILM CAPACITOR	CFTXA103J50		R40-49	CARBONFILM RESISTOR	RD1/6PM□□□J
	C77, 78	(3. 3/25)	PCH1083		R5 CARE	BONFILM RESISTOR	RD1/6PM 🖂 🖂 🖂 J
					R50-55	CARBONFILM RESISTOR	RD1/6PM□□□J
	C79. AUD	DIO FILM CAPACITOR	CFTXA103J50		R57, 58	CARBONFILM RESISTOR	RD1/6PM□□□J
	•	OR FILM CAPACITOR	CQMA223J50				
		AUDIO FILM CAPACITOR	CFTXA103J50		R6 CARE	BONFILM RESISTOR	RD1/6PM□□□J
	•	ELECTR. CAPACITOR	CENA222M16			RBONFILM RESISTOR	RD1/6PM□□□J
		CERAMIC CAPACITOR	CCCSL101J50			CARBONFILM RESISTOR	RD1/6PM□□□J
	223, 20					ONFILM RESISTOR	RD1/2PM2R2J
					-		

Mark	No.	Symbol & Description	Part No.
	D70 CAI	RBONFILM RESISTOR	RD1/6PM□□□J
	KIU CAI	DOMILIAN RESISION	
	R73-79	CARBONFILM RESISTOR	RD1/6PM□□□J
	R8 CARI	BONFILM RESISTOR	RD1/6PM□□□J
	R80-85	CARBONFILM RESISTOR	RD1/6PM□□□J
	R89 CAR	RBONFILM RESISTOR	RD1/6PM□□□J
	R9 CARI	BONFILM RESISTOR	RD1/6PM□□□J
	R90 CAI	RBONFILM RESISTOR	RD1/6PMCCJJ
	R91 CAF	RBONFILM RESISTOR	RD1/2PM2R2J
	R92-99	CARBONFILM RESISTOR	RD1/6PM□□□J
	VR1 VR		VRTB6VS473
	VR2 SEA	MI-FIXED RESISTOR	VRTB6VS103
	VR3-7 V	/R	VRTB6VS223
	VR8 VR		VRTS6VS102
	VR9 VR		VRTB6VS473
OTHE	RS		
	CN301		5597-17CPB
	CN302		5597-05CPB
	DL301, 3	302 FILTER	PTF1009
	JA1201		TOTX174
	JA1202	JACK	PKB1004
	X1 XTAI	RES (OSC)	PSS1001

## 8. ADJUSTMENTS

Perform the following adjustments in the indicated order.

### Adjustments

- Tracking error offset, focus error offset and RF offset adjustment.
- 2. Tracking return offset and focus return offset adjustment.
- 3. Focus lock and spindle lock check.
- 4. Grating adjustment.
- 5. Tracking balance adjustment.
- 6. Tangential adjustment
- 7. Radial adjustment
- 8. RF level check
- 9. LD (laser diode) power check
- 10. Focus gain adjustment
- 11. Tracking gain adjustment
- 12. VCO free-running frequency adjustment
- 13. Method of focus error check
- 14. D/A converter adjustment

### Measuring Devices

- 1. Dual-trace oscilloscope
- 2. Light power meter
- 3. YEDS-7 test disc
- 4. Focus and tracking adjustment filter
- 5. Loop gain adjustment band-pass filter
- 6. Signal generator
- 7. Grating driver
- 8. General-use tools
- 9. Commercial available disc (8 cm and 12 cm)

**Note)** The volumes of VR500 to VR503 and VR501 of D/A converter section are adjusted when shipping, therefore, do not touch or adjust them.

(Adjust them when D/A converter IC (PCM63MP-K) is exchanged.

#### Test Mode

### -- Setting and Canceling the Test Mode

- ① Turn the POWER switch (S1000) while pressing the test mode switch (S800).
- (2) To cancel the test mode, turn the POWER switch OFF.

In the test mode, each key works as shown in Table 1.

#### Adjustment Volume Name

VR1: Focus return offset (FR. OF)

VR2: RF offset (RF. OF)

VR3: Focus gain (FO. GA)

VR4: Tracking gain (TR. GA)

VR5: Tracking balance (TR. BL)

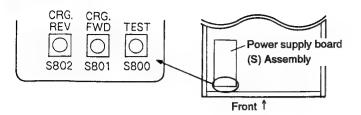
VR6: Focus error offset (FO. OF)

VR7: Tracking error offset (TE. OF)

VR8: VCO frequency counter (VCOA)

VR9: Tracking return offset (TR. OF)





Note) In PD-93, MANUAL SEARCH is not set on the front panel. Therefore, use the switches of the power supply board (S) assembly when moving the carriage in the test mode.

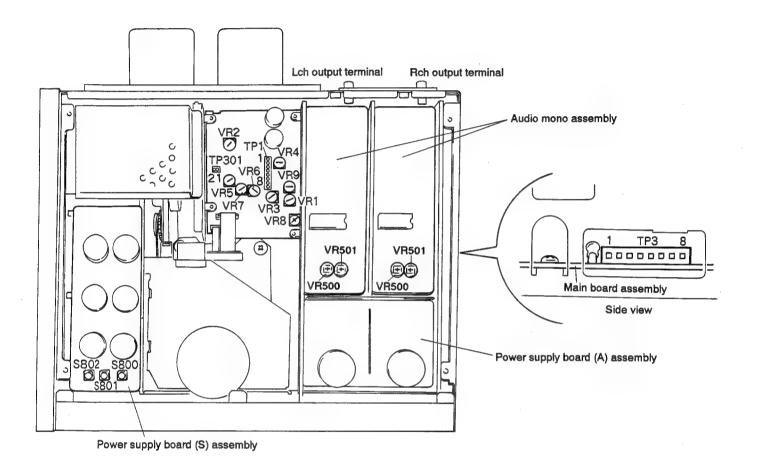
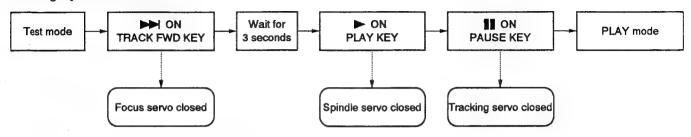


Fig. 1. Adjusting Points

In the test mode, closing and opening of servos is performed independently. Therefore, to set the play mode the servos have to be closed in (serial) sequence. Remember that in the test mode the play mode can't be set simply by pressing PAUSE ( ) key.

For example, to set the play mode from the stop mode, press the following keys in the indicated order.



<sup>\*</sup> In the test mode, servos keep a serial sequence.

### Function of Each Key in the Test Mode

Symbol	Symbol Key name Function test		Description		
44	TRACK BACK	Laser diode ON	Lights the laser diode,		
<b>&gt;&gt;</b>	TRACK FWD	Focus servo close	Lights the laser diode and sets the focus actuator UP/DOWN to close the focus servo.		
<b>&gt;</b>	PLAY	Spindle servo close	After kicking the spindle motor, it closes the servo in the CLV-H mode.		
H	PAUSE	Tracking servo close/open	Performs a toggle operation. When pressed, the tracking serve closed and the unit enters the play mode (the focus serve and spin serve should be already closed). At this time the PAUSE indic lights. If pressed again, the tracking serve opens.		
S802	CRG. REV	Carriage reverse (inward)	Moves the carriage inwards at high (approx. 1 cm/s) speed. Since there is no safety device to stop the carriage, be sure to stop it manually in time.		
\$801	CRG. FWD	Carriage forward (outward)	Moves the carriage outwards at high (approx. 1 cm/s) speed. Since there is no safety device to stop the carriage, be sure to stop it manually in time.		
	STOP	Stop	Stops all servos and returns the unit to the initial condition.		
<b>A</b>	OPEN/CLOSE	(Disc tray) open/close	Opens and closes the disc tray. However, the pickup does not return to the rest position when the tray is opened. It does not move either when the tray is closed.		

Table 1.



Step No.	Oscilloscope setting			Adjusting		Adjustment procedure
	V	Н		points	specifications	
1	Tracking	error offse	t, focus error	offset and R	F offset adjustn	nent
		TP1	TP1 Pin 4 (TR. ER)	VR7 (TE. OF)	0V ± 50 mV	<ul> <li>Set the test mode. (*)</li> <li>Adjust VR7 (TE. OF: tracking error offset so that the voltage at Pin 4 (TE: tracking error) of TP1 becomes 0V ± 50 mV.</li> </ul>
		TP1	TP1 Pin 6 (FO. ER)	VR6 (FO.OF)	0V ± 50 mV	<ul> <li>Adjust VR6 (FO. OF: focus error offset) so that the voltage at Pin 6 (FO. ER: focus</li> </ul>
		TP301	TP301 Pin 1 (RF)	VR2 (RF. OF)	100 mV ± 50 mV	error) of TP1 becomes 0V ± 50 mV.  ■ Adjust VR2 (RF. OF: RF offset) so that RF output voltage at Pin 1 of TP301 becomes 100 mV ± 50 mV.
2	Tracking	return offs	et and focus	return offset	adjustment	
		TP1	TP1 Pin 2 (TR. RT)	VR9 (TR. OF)	0V ± 10 mV	<ul> <li>Set the test mode. (*)</li> <li>Adjust VR9 (TR. OF: tracking return offset) so that the voltage at Pin 2 TR. RT (tracking return) of TP1 becomes 0V ± 10 mV.</li> </ul>
		TP1	TP1 Pin 8 (FO. RT)	VR1 (FR. OF)	35 mV ± 17.5 mV	<ul> <li>Adjust VR1 (FR. OF: focus return offset) so that the voltage at Pin 8 FO. RT (focus return) of TP1 becomes 35 mV ± 17.5 mV.</li> </ul>
3	Focus loc	k and spin	dle lock chec	k		
	V 0.5V/div	H 100 msec /div	TP301 Pin 1 (RF output)		RF output  Clockwise rotation	<ul> <li>Load the disc.</li> <li>Set the test mode. (*)</li> <li>Move the pickup close to the center of the disc using CRG. FWD key (S801). Be sure to perform this operation.</li> <li>Observe Pin 1 RF (RF output) of TP301 with an oscilloscope and confirm that RF signal is output after pressing TRACK FWD key (▶►).</li> <li>Press PLAY key (▶) and confirm that the disc rotates clockwise at approx. normal</li> </ul>
						speed (about 300 rpm around the center of the disc), without running wildly or in reverse direction.

<sup>\*</sup> See page 49.

Step	Oscilloso	cope setting	Test points	Adjusting	Check items/ adjustment	Adjustment procedure
No.	٧	Н		points	specifications	
4-1	Grating a	adjustment	(1) (with an 8	cm disc)		
	1	5 ms/div	TP1 Pin 4 (TR. ER)		Null point	<ul> <li>This adjustment can be performed with an 8 cm disc having pits over a 75 mm in diameter.</li> <li>Load the disc. (8 cm)</li> <li>Set the test mode. (*)</li> <li>Press TRACK FWD (▶►) and PLAY (▶) keys in that order to close the focus and spindle servos (the tracking servo is open state.)</li> <li>Press CRG. FWD key (S801) and move the pickup to the outer track of the 8 cm disc. When moving the pickup, it is possible to insert a slotted screwdriver in the grating adjustment plate slot from above the unit. (Fig. 3.)</li> <li>Observe the waveform at Pin 4 TR. ER (tracking error) of TP1 with an oscilloscope and at this time, insert cut off 4 kHz low-pass filter (Fig. 2).</li> <li>Insert the tracking driver in the adjustment slot and turn if so as to find out the null point (Photo-1).</li> </ul>
		Pin5 O	0.001	μF		
		(GND)	; 	i		
			Fig. 2	•		
,	5 mV/div	XY	X axis: R328 Y axis: R327	Grating Grating	Maximum amplitude  Phase difference 180°	<ul> <li>Turn the grating driver slowly clockwise from the null point and set to the first point where the waveform amplitude (tracking error signal) is maximum. (See photo-2.)</li> <li>Connect CN301 (PDF) of R328 to X axis of an oscilloscope and CN301 (PDE) of R327 to Y axis on inserting the filters of about 4 kHz of cutoff respectively. Move the pickup to the most outer track of 8 cm disc.</li> </ul>
		CN301 Pin9 (PDE) Pin5 (PDF)	W R328	1000P 10	Y axis  IC301  X axis	At this time, check that the resurge waveform almost is one line, if not adjust. (Photos-4, 5)

\* See page 49.



Step No.		ope setting	Test points	Adjusting points	Check items/ adjustment	Adjustment procedure
4–2	Croting	H	(2) (with on 1	2 om dige pla	specifications  lying more than	60 minutes)
	1V/div	5 ms/div	TP1 Pin 4 (TR. ER)  L.P.F.  39k Ω  0.001	Grating	Null point	<ul> <li>Load the disc (playing more than 60 minutes).</li> <li>Set the test mode. (*)</li> <li>Press TRACK FWD (▶►) and PLAY (▶) keys in that order to close the focus and spindle servos (the tracking servo is open state).</li> <li>Press CRG. FWD key (S801) and move the pickup to the outer track of the disc. When moving the pickup, it is possible to insert a slotted screwdriver in the grating adjustment plate slot from above the unit. (Fig. 3.)</li> <li>Observe the waveform at Pin 4 TR. ER (tracking error) of TP1 with an oscilloscope and at this time, insert cut off 4 kHz low-pass filter. (Fig. 2.)</li> <li>Insert the tracking driver in the adjustment slot and turn it so as to find out the null point (Photo-1).</li> </ul>
			Fig. 2.	,		
				Grating	Maximum amplitude	● Turn the grating driver slowly clockwise from the null point and set to at the first point where the waveform amplitude (tracking error signal) is maximum. (See photo-2)
	5 mV/div	5 ms/div	X axis: R328 Y axis: R327	Grating	Phase difference 180°	● Connect CN301 (PDF) of R328 to X axis of an oscilloscope and CN301 (PDE) of R327 to Y axis to input with AC coupling, and then move the pickup close to the center track of the disc. At this time, adjust so that the resurge waveform almost becomes one line, if not, adjust in the outer track again. (Photos—4, 5)

<sup>\*</sup> See page 49.

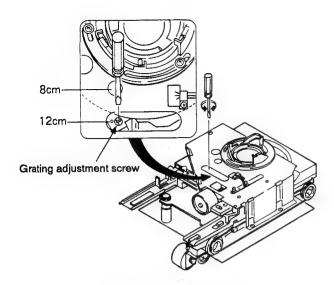
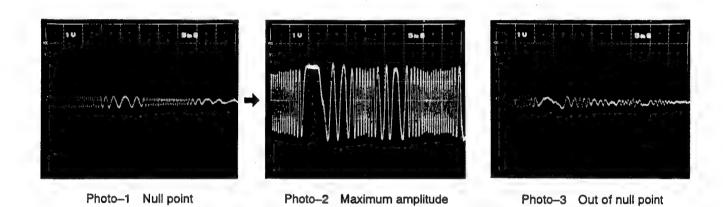
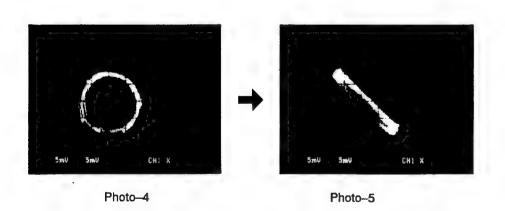


Fig. 3. Grating Adjustment







Step	Oscillos	cope setting	Test points	Adjusting	Check items/ adjustment	Adjustment procedure
No.	٧	н	1 Tout points	points	specifications	Aujustinent procedure
5	Tracking	g balance ac	ljustment			· .
	0.5V/div	5 msec/div	TP1 Pin 4 (TR. ER)	VR5 (TR. BL)	- A -+ B -	<ul> <li>Load the disc.</li> <li>Set the test mode. (*)</li> <li>Press CRG. FWD key (S801) and move carriage close to the center track of the disc.</li> <li>Press TRACK FWD (▶►) and PLAY (▶) keys in that order to turn the disc.</li> <li>Observe Pin 4 TR. ER (tracking error) of TP1 with an oscilloscope. And adjust VR5 TR. BL (tracking balance) so as to remove DC elements from the tracking error waveform.</li> </ul>
			Photo-6			Photo-7

<sup>\*</sup> See page 49.

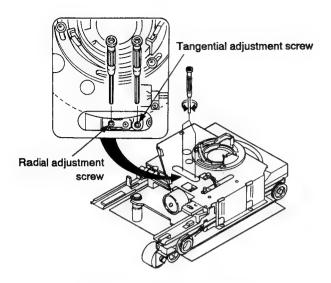
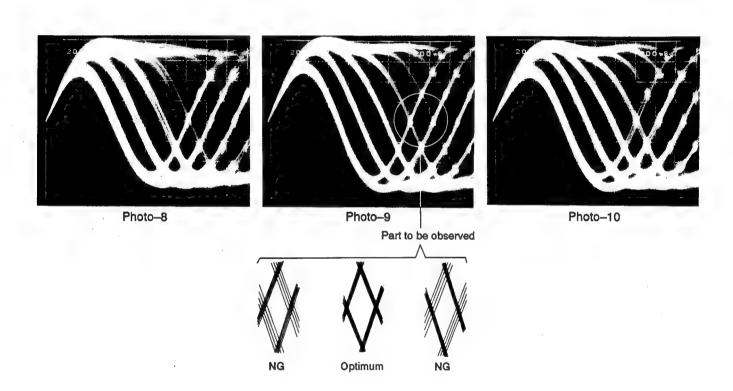


Fig. 4. Tangential Adjustment





Step	Oscillosco	ope setting	Test points	Adjusting	Check items/	Adjustment procedure
No.	٧	н		points	specifications	Adjustinos proceduro
6	Tangentia	al adjustme	ent			
			TP301 Pin 1 (RF output)	Tangential adjustment screw	Eye pattern optimum point	<ul> <li>Load the disc.</li> <li>Set the test mode. (*)</li> <li>Press CRG. FWD key (S801) and move the pickup to the center track of the disc (set it to such a location that the tangential screw can be seen from above the servo mechanism. (See fig. 4.)</li> <li>Press TRACK FWD (▶►), PLAY (▶) and PAUSE (II) keys in that order to close all servos. (Pause indicator lights.)</li> <li>Observe Pin 1 RF (RF output) of TP301 with an oscilloscope and adjust the tangential screw so that the eye pattern becomes clear. (Fig. 4.)</li> <li>The adjustment point is located around the middle location between the point where the eye pattern becomes blurred when turning the tangential screw clockwise and the point where the eye pattern becomes blurred when turning the adjustment screw counterclockwise.</li> <li>Observe the overall clearness of the waveform and one of the diamond shapes in the eye pattern (photo-9). Optimum adjustment is attained at the point where diamond shape lines are relatively thin.</li> </ul>
					A.	
					PinI (RF)	
					Pin2 (GND)	Fig. 5

<sup>\*</sup> See page 49.



Step	Oscillosco	pe setting	_ Test points	Adjusting	Check items/ adjustment	Adjustment procedure
No.	٧	н	Took points	points	specifications	Adjustitions procedure
7	Radial ad	ustment				
	,		TP301 Pin 1 (RF output)	Radial adjustment screw	Eye pattern optimum point	<ul> <li>Load the disc.</li> <li>Set the test mode. (*)</li> <li>Press CRG. FWD key (S801) and move the pickup to the center track of the disc (set it to such a location that the tangential screw can be seen from above the servo mechanism. (See fig. 4.)</li> <li>Press TRACK FWD (▶►), PLAY (▶) and PAUSE (▮) keys in that order to close all servos. (Pause indicator lights.)</li> <li>Observe Pin 1 RF (RF output) of TP301 with an oscilloscope and adjust the tangential screw so that the eye pattern becomes clear. (Fig. 4.)</li> <li>The adjustment point is located around the middle location between the point where the eye pattern becomes blurred when turning the tangential screw clockwise and the point where the eye pattern becomes blurred when turning the adjustment screw counterclockwise.</li> <li>Observe the overall cleamess of the waveform and one of the diamond shapes in the eye pattern (photo-9). Optimum adjustment is attained at the point where diamond shape lines are relatively thin.</li> <li>Perform the tangential and radial adjustments alternately two or more times.</li> </ul>
					Pin1 (RF) Pin2 (GND)	
						Fig. 5

\* See page 49.



Step	Oscilloscope setting			Adjusting	- adiliatment	Adjustment procedure
No.	v	Н		points	specifications	
8	RF level c	heck				
			TP301 Pin 1 (RF) TP301 Pin 1 (RF)	Check VR (A)	1.5V <sup>+0.2V</sup> 1.5V <sup>+0.2V</sup>	<ul> <li>Set the test mode. (*)</li> <li>Connect the probe of the oscilloscope to Pin 1 RF (RF output) of TP301.</li> <li>Play back the disc, measure the RF waveform p-p voltage and confirm that it becomes 1.5V <sup>+0.2V</sup><sub>-0V</sub>.</li> <li>Adjust VR A if the voltage does not become 1.5V <sup>+0.2V</sup><sub>-0V</sub>.</li> </ul>
9	LD (Laser	diode) po	wer check			
				VR®	Specified value 0.13 mW or less	<ul> <li>Set the test mode. (*)</li> <li>Press TRACK BACK key ( ◄</li> <li>) and turn the LD (laser diode) on.</li> <li>Place the sensor of the light power meter directly above the objective lens and confirm that LD power is the specified value (0.13 mW or less).</li> </ul>

<sup>\*</sup> See page 49.

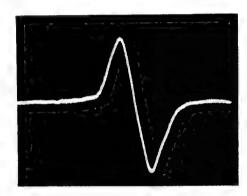
Step	Oscilloscope setting		Test points	Adjusting	Check items/ adjustment	Adjustment procedure	
No.	V	н		points	specifications	Adjustment procedure	
10	Focus ga	ain adjustm	ent				
	CH1 (X) , CH2 (Y) 20 mV/div, 5 mV/div (probe 10:1)		X axis: TP1 Pin 5 (FO. IN) Y axis: TP1 Pin 6 (FO. ER)	VR3 (FO. GA)	Phase difference 90°  Pin7 (F0.IN) Pin5 (GND) Pin6 (F0.ER)	<ul> <li>With the power off, connect the oscilloscope and the oscillator as shown in Fig. 6.</li> <li>Set the normal playback mode.</li> <li>Turn the oscillators power on and set it to output a 1.2 kHz, 1 Vp-p signal.</li> <li>Note: (Some oscillators output DC when turned ON. In that case, High gain connect the oscillator after turning it on.)</li> <li>Adjust VR3 FO. GA (focus gain) so that the resurge waveform on an oscilloscope becomes a horizontal circle (phase difference 90°).</li> </ul>	
					(1	Fig. 6.	
		h gain oto-11		Optimu Photo		Low gain Photo-13	



Step	Oscilloscope setting		Test points Adjusting		Check items/	Adjustment procedure
No.	٧	Н		points	specifications	, , , , , , , , , , , , , , , , , , ,
11	Tracking	gain adjus	stment			
	CH1 (X), CH2 (Y) 50 mV/div, 5 mV/div (Probe 10:1)		X axis: TP1 Pin 3 (TR. IN) Y axis: TP1 Pin 2 (TR. ER)	VR4 (TR. GA)	Phase difference 90°	With the power off, connect the oscilloscope and the oscillator as shown in Fig. 7.      Set the normal playback mode.     Turn the oscillators power on and set it to output a 1 kHz, 2 Vp−p signal.  Note: (Some oscillators output DC when turned on. In that case, connect the oscillator after turning it on.)      Adjust VR4 TR. GA (tracking gain) so that the resurge waveform on an oscilloscop becomes a horizontal circle (phase difference 90°).      OSC
					20k Q	9kΩ 10:1) 10.0012μF Fig. 7.
		h gain		Optimu	_	Low gain
12		oto-14	requency adji	Photo	7-15	Photo-16
	100 1186		i oquonicy auju	131116111		Cot the test made (*)
			TP3 Pin 2		Frequency 4.275 MHz ± 0.025 MHz	<ul> <li>Set the test mode. (*)</li> <li>Connect the frequency counter (10 MH range) to Pin 2 of TP3.</li> <li>Adjust VR8 (VCO. A) so that the frequency counter reads 4.275 MHz ± 0.025 MHz.</li> <li>Note: Adjust with the stop mode.</li> </ul>

<sup>\*</sup> See page 49.

Step	Oscilloscope setting		Test points	Adjusting	Check items/ adjustment	Adjustment procedure
No.	٧	н		points	specifications	
13	Focus er	ror check		·		
	1V/div	2 ms/div	TP1 Pin 6 (FO. ER)	Check	Waveform	<ul> <li>Set the test mode. (*)</li> <li>Connect Pin 7 FO. IN (focus in) of TP1 to GND.</li> <li>Press TRACK FWD key and check the waveform on Pin 6 FO. ER (focus error) of TP1 with the oscilloscope.</li> </ul>



Focus error Photo-17

14	D/A converter adju	D/A converter adjustment						
		LINE OUT (L), (R) terminal	VR500 (Lch) VR501 (Lch) VR500 (Rch) VR501 (Rch)	Minimum distortion ratio	<ul> <li>Set the test disc (YEDS-7) and connect the distortion meter to LINE OUT (L or R) of the main unit.</li> <li>Play back the 1 kHz/0 dB signal and adjust VR500 and VR501 alternately so that the distortion ratio becomes minimum.</li> </ul>			

Note: When a distortion meter is not available, cut the lead wires of R500 and R501 of the AUDIO MONAURAL BOARD ASSEMBLY using nippers and remove the resistors.

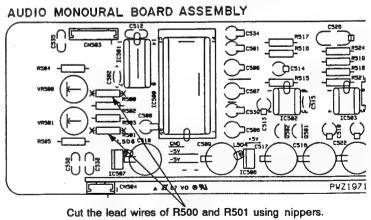


Fig. 8.

<sup>\*</sup> See page 49.



# 9. IC DESCRIPTIONS (CXD1165Q)

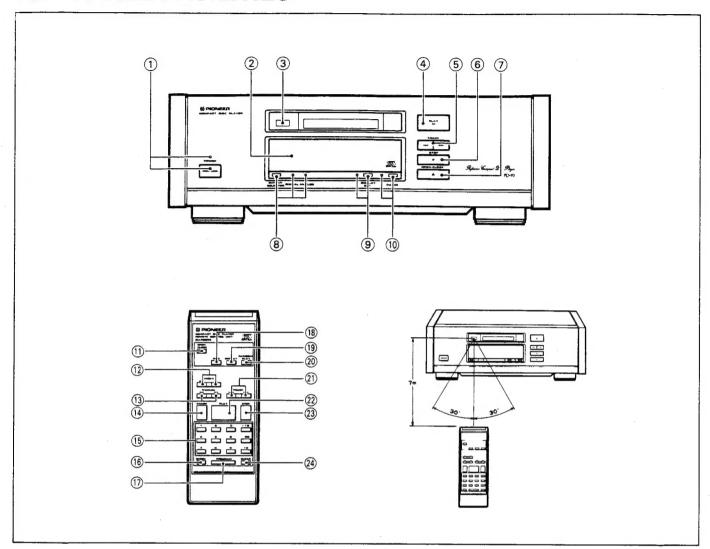
# Pin Descriptions

1 FSW O Time constant selection output of spindle motor output filter. 2 MON O ON/OFF control output of spindle motor. 3 MDP O Drive output of spindle motor, rough control when CLV-S mode and phase control when CLV-P mode. 4 MDS O Drive output of spindle motor, speed control when CLV-P mode. 5 EFM I EFM signal input from RF amplifier. 6 ASY O Output to control slice level of EFM signal. "L" when stop. 7 LOCK O After sampling GFS signal in WFCK/16, if it is "H", "H" is output, and if it is "L" for eight successive times, "L" is output. 8 VCOO O VCO output, F=8,6436 MHz when locked to EFM signal. 9 VCOI I VCO output, F=8,6436 MHz when locked to EFM signal. 10 TEST I (OV) 11 PDO O Phase comparison output (compared EFM signal with VCO/2), "Hi-Z" when stop. 12 Vss - GND (OV) 13 CLK I, Serial data transfer clock input from CPU, Data is latched at rising edge of clock. 14 XLT I Latch input from CPU, B bits shift register data (serial data from CPU) are latched to the respective registers. 15 DATA I Serial data input from CPU. 16 XRST I System reset input, reset when "L". 17 CNIN I Tracking pulse input. 18 SENS O Cutputs internal condition according to address. 19 MUTG I Mutting input, when ATTM of internal register A is "L", it is normal state at MUTG "L', and no sound state at "H". 20 CRCF O Outputs CRC check results of sub code. 22 SBSO O Serial output of sub code. 23 SUBO O Sub code Output. 24 SCOR O SO+SI output of sub code. 25 SQCK I COkok input for serial output of sub code. 26 SGCX I SQCK selection input. Refer to 1CPU interface. 27 DOTX O Digital output of lock state for frame sync. 28 FEST SQCK I Clock input for serial output of sub code. 39 TEST SQCK I Clock input for serial output of sub code. 30 TEST SQCK I SQCK selection input. Refer to 1CPU interface. 31 TEST SQCK I SQCK selection input. Refer to 1CPU interface. 31 TEST SQCK I SQCK selection input. Refer to 1CPU interface. 31 TEST SQCK I SQCK selection input. Refer to 1CPU interface. 32 TEST SQCK I SQCK selection input. Refer to 1CPU interface. 33 TEST S	Pin No.	Name	1/0	Descriptions
MDP 0 Drive output of spindle motor, rough control when CLV-S mode and phase control when CLV-P mode.  4 MDS 0 Drive output of spindle motor, speed control when CLV-P mode.  5 EPM 1 EFM signal input from RF amplifier.  6 ASY 0 Output to control slibe level of EFM signal, "L" when stop.  7 LOCK 0 After sampling GFS signal in WFCK/16, if it is "H", "H" is output, and if it is "L" for eight successive times, "L" is output.  8 VOOO 0 VCO output, F=8.6436 MHz when locked to EFM signal.  9 VCOI 1 VCO output, F=8.6436 MHz when locked to EFM signal.  10 TEST 1 (OV)  11 PDO 0 Phase comparison output (compared EFM signal with VCO/2), "Hi-Z" when stop.  12 Vss - GND (OV)  13 CLK 1, Serial data transfer clock input from CPU, Data is latched at rising edge of clock.  14 XLT 1 Latch input from CPU, B bits shift register data (serial data from CPU) are latched to the respective registers.  15 DATA 1 Serial data input from CPU.  16 XRST 1 System reset input, reset when "L".  17 CNIN 1 Tracking pulse input.  18 SENS 0 Outputs internal condition according to address.  19 MUTG 1 Mutting input, when ATTM of internal register A is "L", it is normal state at MUTG "L", and no sound state at "H".  20 CRCF 0 Outputs CRC check results of sub code Q.  21 EXCK 1 Clock input for serial output of sub code.  22 SBS0 0 Serial output of sub code.  23 SUBO 0 Sub code Q output.  24 SCOR 0 SO+S1 output of sub code ync.  25 SQCK   // O Read clock of sub code Q.  27 DOTX 0 Digital output (WFCK is output when DO off.)  28 GFS 0 Display output of lock state for frame sync.  1 Fix to "H" or "L". Do not open.  1 Fix to "H" or "L". Do not open.  1 Fix to "H" or "L". Do not open.	1	FSW	0	Time constant selection output of spindle motor output filter.
## MDS O Drive output of spindle motor, speed control when CLV-P mode.  ## MDS O Drive output of spindle motor, speed control when CLV-P mode.  ## MDS O Drive output form RF amplifier.  ## CO O O Utput to control slice level of EFM signal, "L" when stop.  ## LOCK O After sampling GFS signal in WFCK/T-I6, if it is "H", "H" is output, and if it is "L" for eight successive times, "L" is output.  ## VCOO O VCO output, f=8.6436 MHz when locked to EFM signal.  ## VCOO I I VCO input.  ## VCOI I VCOI Input.  ## VCOI Input.  ## VCOI I VCOI Input.  ## VCOI Input.  ## VCOI I VCOI Input.  ## VCOI I VCOI Input.  ## VCOI Input.  ## VCOI I VCOI Input.  ## VCOI Input	2	MON	0	ON/OFF control output of spindle motor.
EFM	3	MDP	0	
ASY O Cutput to control slice level of EFM signal, "L" when stop.  After sampling GFS signal in WFCK_7[6, if it is "H", "H" is output, and if it is "L" for eight successive times, "L" is output.  B VCOO O VCO output, f=8.6436 MHz when locked to EFM signal.  9 VCOI I VCO input.  10 TEST I (0V)  11 PDO O Phase comparison output (compared EFM signal with VCO_2), "Hi-Z" when stop.  12 Vss — GND (0V)  13 CLK I Serial data transfer clock input from CPU, Data is latched at rising edge of clock.  14 XLT I Latch input from CPU, B bits shift register data (serial data from CPU) are latched to the respective registers.  15 DATA I Serial data input from CPU.  16 XRST I System reset input, reset when "L".  17 CNIN I Tracking pulse input.  18 SENS O Outputs internal condition according to address.  Mutrg I Mutrg I Mutrg input, when ATTM of internal register A is "L", it is normal state at MUTG "L', and no sound state at "H".  20 CRCF O Outputs CRC check results of sub code Q.  21 EXCK I Clock input for serial qutput of sub code.  22 SESO O Serial output of sub code.  23 SUBQ O Sub code Q output.  24 SCOR O SO+SI output of sub code.  25 SQCK I/O Read clock of sub code Q.  26 SQEX I SQCK selection input, Refer to 1CPU interface.  27 DOTX O Display output of lock state for frame sync.  Fix to "H" or "L". Do not open.  Fix to "H" or "L". Do not open.  Fix TEST ST TEST S	4	MDS	0	Drive output of spindle motor, speed control when CLV-P mode.
After sampling GFS signal in WFCK_/16, if it is "H", "H" is output, and if it is "L" for eight successive times, "L" is output.  8	5	EFM		EFM signal input from RF amplifier.
Boundary   Color   C	6	ASY	0	Output to control slice level of EFM signal, "L" when stop.
9 VCOI   VCO input. 10 TEST   (OV) 11 PDO   O Phase comparison output (compared EFM signal with VCO/2), "Hi-Z" when stop. 12 Vss   GND (OV) 13 CLK   Serial data transfer clock input from CPU. Data is latched at rising edge of clock. 14 XLT   Latch input from CPU, B bits shift register data (serial data from CPU) are latched to the respective registers. 15 DATA   Serial data input from CPU. 16 XRST   System reset input, reset when "L". 17 CNIN   Tracking pulse input. 18 SENS   O Cutputs internal condition according to address. 19 MUTG   Muting input, when ATTM of internal register A is "L", it is normal state at MUTG "L", and no sound state at "H". 20 CRCF   O Cutputs CRC check results of sub code Q. 21 EXCK   Clock input for serial output of sub code. 23 SUBQ   O Sub code Q output. 24 SCOR   O Serial output of sub code sync. 25 SQCK   I Clock input for sub code sync. 26 SQCX   SQCK   SQCK of Sub code C output. 27 DOTX   O Digital output of lock state for frame sync. 28 GFS   O Display output of lock state for frame sync. 29 TEST   Fix to "H" or "L". Do not open. 30 TEST   Fix to "H" or "L". Do not open. 31 TEST   Fix to "H" or "L". Do not open. 33 TEST   Fix to "H" or "L". Do not open.	7	LOCK	0	
10 TEST I (OV) 11 PDO O Phase comparison output (compared EFM signal with VCO/2), "Hi-Z" when stop. 12 Vss — GND (OV) 13 CLK I Serial data transfer clock input from CPU, Data is latched at rising edge of clock. 14 XLT I Latch input from CPU, 8 bits shift register data (serial data from CPU) are latched to the respective registers. 15 DATA I Serial data input from CPU. 16 XRST I System reset input, reset when "L". 17 CNIN I Tracking pulse input. 18 SENS O Outputs internal condition according to address. 19 MUTG I Muting input, when ATTM of internal register A is "L", it is normal state at MUTG "L", and no sound state at "H". 20 CRCF O Outputs CRC check results of sub code Q. 21 EXCK I Clock input for serial output of sub code. 22 SBSO O Serial output of sub code. 23 SUBQ O Sub code Q output. 24 SCOR O SO+S1 output of sub code SUC. 25 SQCK I/O Read clock of sub code Q. 26 SQEX I SQCK selection input. Refer to 1CPU interface. 27 DOTX O Digital output (WFCK is output when DO off.) 28 GFS O Display output of lock state for frame sync. 29 TEST I Fix to "H" or "L". Do not open. 30 TEST I Fix to "H" or "L". Do not open. 31 TEST I Fix to "H" or "L". Do not open. 32 TEST I Fix to "H" or "L". Do not open.	8	VC00	0	VCO output, f=8.6436 MHz when locked to EFM signal.
11 PDO O Phase comparison output (compared EFM signal with VCO/2), "Hi-Z" when stop.  12 Vss — GND (0V)  13 CLK I. Serial data transfer clock input from CPU. Data is latched at rising edge of clock.  14 XLT I Latch input from CPU. B bits shift register data (serial data from CPU) are latched to the respective registers.  15 DATA I. Serial data input from CPU.  16 XRST I. System reset input, reset when "L",  17 CNIN I. Tracking pulse input.  18 SENS O Outputs internal condition according to address.  19 MUTG I Muting input, when ATTM of internal register A is "L", it is normal state at MUTG "L", and no sound state at "H".  20 CRCF O Outputs CRC check results of sub code Q.  21 EXCK I. Clock input for serial output of sub code.  22 SBSO O Serial output of sub code.  23 SUBQ O Sub code Q output.  24 SCOR O SO+SI output of sub code sync.  25 SQCK I/O Read clock of sub code Q.  26 SQEX I. SQCK selection input, Refer to 1CPU interface.  27 DOTX O Digital output (WFCK is output when DO oft).  29 TEST  30 TEST  31 TEST  31 TEST  33 Vob — Power supply (+5V)  34 TEST  35 TEST  36 TEST  37 TEST  38 TEST  39 TEST  39 TEST  39 TEST	9	VCOI .	1	VCO input.
12  Vss	10	TEST	ļ	
13 CLK I Serial data transfer clock input from CPU. Data is latched at rising edge of clock.  14 XLT I Latch input from CPU, B bits shift register data (serial data from CPU) are latched to the respective registers.  15 DATA I Serial data input from CPU.  16 XRST I System reset input, reset when "L".  17 CNIN I Tracking pulse input.  18 SENS O Outputs internal condition according to address.  19 MUTG I Muting input, when ATTM of internal register A is "L", it is normal state at MUTG "L", and no sound state at "H".  20 CRCF O Outputs CRC check results of sub code Q.  21 EXCK I Clock input for serial qutput of sub code.  22 SBSO O Serial output of sub code.  23 SUBQ O Sub code Q output.  24 SCOR O SO+51 output of sub code sync.  25 SQCK I/O Read clock of sub code Q.  26 SQEX I SQCK selection input. Refer to 1CPU interface.  27 DOTX O Digital output (WFCK is output when DO off.)  28 GFS O Display output of lock state for frame sync.  29 TEST O Display output of lock state for frame sync.  1 Fix to "H" or "L". Do not open.  1 Fix to "H" or "L". Do not open.  2 Fix TEST O Power supply (+5V)	11	PDO	0	Phase comparison output (compared EFM signal with VCO/2), "Hi-Z" when stop.
Latch input from CPU, B bits shift register data (serial data from CPU) are latched to the respective registers.    Serial data input from CPU.		Vss	_	GND (OV)
respective registers.  15 DATA I Serial data input from CPU.  16 XRST I System reset input, reset when "L".  17 CNIN I Tracking pulse input.  18 SENS O Outputs internal condition according to address.  19 MUTG I Muting input, when ATTM of internal register A is "L", it is normal state at MUTG "L", and no sound state at "H".  20 CRCF O Outputs CRC check results of sub code Q.  21 EXCK I Clock input for serial output of sub code.  22 SBSO O Serial output of sub code.  23 SUBQ O Sub code Q output.  24 SCOR O SO+S1 output of sub code sync.  25 SQCK I/O Read clock of sub code Q.  26 SQEX I SQCK selection input. Refer to 1CPU interface.  27 DOTX O Digital output (WFCK is output when DO off.)  28 GFS O Display output of lock state for frame sync.  30 TEST I Fix to "H" or "L". Do not open.  31 TEST I Fix to "H" or "L". Do not open.  33 Veb - Power supply (+5V)  34 TEST I Fix to "H" or "L". Do not open.  35 TEST I Fix to "H" or "L". Do not open.	13	CLK	1.	Serial data transfer clock input from CPU, Data is latched at rising edge of clock.
16 XRST I System reset input, reset when "L".  17 CNIN I Tracking pulse input.  18 SENS O Outputs internal condition according to address.  19 MUTG I Muting input, when ATTM of internal register A is "L", it is normal state at MUTG "L", and no sound state at "H".  20 CRCF O Outputs CRC check results of sub code Q.  21 EXCK I Clock input for serial output of sub code.  22 SBSO O Serial output of sub code.  23 SUBQ O Sub code Q output.  24 SCOR O SO+S1 output of sub code sync.  25 SQCK I/O Read clock of sub code Q.  26 SQEX I SQCK selection input. Refer to 1CPU interface.  27 DOTX O Digital output (WFCK is output when DO off.)  28 GFS O Display output of lock state for frame sync.  29 TEST 30 TEST I Fix to "H" or "L". Do not open.  31 TEST 33 Vop — Power supply (+5V)  34 TEST 35 TEST 37 TEST I Fix to "H" or "L". Do not open.  38 TEST 39 TEST 37 TEST 37 TEST 39 TEST 30 TEST 39 TEST 30 TEST 39 TEST 30 TEST 39 TEST 30 TEST 39	14	XLT	1	
17 CNIN I Tracking pulse input.  18 SENS O Outputs internal condition according to address.  19 MUTG I Muting input, when ATTM of internal register A is "L", it is normal state at MUTG "L", and no sound state at "H".  20 CRCF O Outputs CRC check results of sub code Q.  21 EXCK I Clock input for serial output of sub code.  22 SBSO O Serial output of sub code.  23 SUBQ O Sub code Q output.  24 SCOR O SO+S1 output of sub code sync.  25 SQCK I/O Read clock of sub code Q.  26 SQEX I SQCK selection input. Refer to 1CPU interface.  27 DOTX O Digital output (WFCK is output when DO off.)  28 GFS O Display output of lock state for frame sync.  29 TEST  30 TEST  31 TEST  33 Vob - Power supply (+5V)  34 TEST  36 TEST  37 TEST  38 TEST  39 TEST  39 TEST  39 TEST  39 TEST	15	DATA	1	Serial data input from CPU.
18 SENS O Outputs internal condition according to address.  19 MUTG I Muting input, when ATTM of internal register A is "L", it is normal state at MUTG "L", and no sound state at "H".  20 CRCF O Outputs CRC check results of sub code Q.  21 EXCK I Clock input for serial output of sub code.  22 SBSO O Serial output of sub code.  23 SUBQ O Sub code Q output.  24 SCOR O SO+S1 output of sub code sync.  25 SQCK I/O Read clock of sub code Q.  26 SQEX I SQCK selection input. Refer to 1CPU interface.  27 DOTX O Digital output (WFCK is output when DO off.)  28 GFS O Display output of lock state for frame sync.  29 TEST ST S	16	XRST		System reset input, reset when "L".
MUTG I Muting input, when ATTM of internal register A is "L", it is normal state at MUTG "L", and no sound state at "H".  20 CRCF O Outputs CRC check results of sub code Q.  21 EXCK I Clock input for serial output of sub code.  22 SBSO O Serial output of sub code.  23 SUBQ O Sub code Q output.  24 SOOR O SO+S1 output of sub code sync.  25 SQCK I/O Read clock of sub code Q.  26 SQEX I SQCK selection input. Refer to 1CPU interface.  27 DOTX O Digital output (WFCK is output when DO off.)  28 GFS O Display output of lock state for frame sync.  29 TEST 30 TEST I Fix to "H" or "L". Do not open.  32 TEST 35 TEST 36 TEST 37 TEST I Fix to "H" or "L". Do not open.  38 TEST 39 TEST I Fix to "H" or "L". Do not open.	17	CNIN		Tracking pulse input.
and no sound state at "H".	18	SENS	0	
21 EXCK   Clock input for serial output of sub code.  22 SBSO   O Serial output of sub code.  23 SUBQ   O Sub code Q output.  24 SCOR   O SO+S1 output of sub code sync.  25 SQCK   I/O   Read clock of sub code Q.  26 SQEX   I SQCK selection input. Refer to 1CPU interface.  27 DOTX   O Digital output (WFCK is output when DO off.)  28 GFS   O Display output of lock state for frame sync.  29 TEST   I Fix to "H" or "L". Do not open.  30 TEST   Fix to "H" or "L". Do not open.  31 TEST   Fix to "H" or "L". Do not open.  32 TEST   Fix to "H" or "L". Do not open.	19	митс	ı	
22 SBSO	20	CRCF	0	Outputs CRC check results of sub code Q.
SUBQ		EXCK		Clock input for serial output of sub code.
24 SCOR O S0+S1 output of sub code sync. 25 SQCK I/O Read clock of sub code Q. 26 SQEX I SQCK selection input. Refer to 1CPU interface. 27 DOTX O Digital output (WFCK is output when DO off.) 28 GFS O Display output of lock state for frame sync. 29 TEST 30 TEST 31 TEST 32 TEST 33 Vop - Power supply (+5V) 34 TEST 35 TEST 36 TEST 37 TEST 38 TEST 39 TEST				
25   SQCK   I				
26 SQEX I SQCK selection input. Refer to 1CPU interface.  27 DOTX O Digital output (WFCK is output when DO off.)  28 GFS O Display output of lock state for frame sync.  29 TEST 30 TEST 31 TEST 32 TEST 33 Vop - Power supply (+5V)  34 TEST 35 TEST 36 TEST 37 TEST I Fix to "H" or "L". Do not open.  38 TEST 39 TEST				
27         DOTX         O         Digital output (WFCK is output when DO off.)           28         GFS         O         Display output of lock state for frame sync.           29         TEST         Image: Test of the sync output when DO off.)           30         TEST         Image: Test of the sync output when DO off.)           31         TEST         Image: Test of the sync output when DO off.)           31         TEST         Image: Test of the sync output when DO off.)           32         TEST         Image: Test of the sync output when DO off.)           33         Test of the sync output of lock state for frame sync.           34         TEST         Image: Test of the sync output of lock state for frame sync.           34         TEST         Image: Test of the sync output of lock state for frame sync.           35         TEST         Image: Test of the sync output of lock state for frame sync.           36         TEST         Image: Test of the sync output of lock state for frame sync.           37         TEST         Image: Test of the sync output of lock state for frame sync.           38         TEST         Image: Test of the sync output of lock state for frame sync.           39         TEST         Image: Test of the sync output of lock state for frame sync.           30         TEST			1/0	
28 GFS				
29 TEST 30 TEST 31 TEST 31 TEST 32 TEST 33 Vob — Power supply (+5V) 34 TEST 35 TEST 36 TEST 37 TEST 39 TEST 39 TEST				
30 TEST			0	Display output of lock state for frame sync.
31 TEST				
31   TEST   32   TEST   33   Vob   -   Power supply (+5V)   34   TEST   35   TEST   36   TEST   37   TEST   1   Fix to "H" or "L". Do not open.   38   TEST   39   TEST			1 .	Fix to "H" or "L". Do not open.
33				
34 TEST 35 TEST 36 TEST 37 TEST 1 Fix to "H" or "L". Do not open. 38 TEST 39 TEST				Device county († 5V)
35 TEST 36 TEST 37 TEST 1 Fix to "H" or "L". Do not open. 38 TEST 39 TEST				rower supply (+5V)
36 TEST   Fix to "H" or "L". Do not open. 38 TEST				
37 TEST   Fix to "H" or "L". Do not open. 38 TEST				
38 TEST			ı	Fix to "H" or "I" Do not open
39 TEST				Trix to Fig. 1. Do not open.
	40	TEST		

Pin			
No.	Name	1/0	Descriptions
	TEST		
41			
42	TEST		
43	TEST		
44	TEST		
45	TEST	1	Fix to "H" or "L". Do not open.
46	TEST		
47	TEST		·
48	TEST		
50	TEST		
51	C4M	0	Crystal dividing frequency output, f=4.2336 MHz.
52	Vss	_	GND (0V)
53	XTAI	1	Crystal oscillation circuit input, f=8.4672 MHz or 16.9344 MHz by mode selection.
54	XTAO	0	Crystal oscillation circuit output, f=8.4672 MHz or 16.9344 MHz by mode selection.
55	MD1	i	Mode selection input 1
56	MD2	<del></del>	Mode selection input 2
57	MD3		Mode selection input 3
-37	IVIUS	'	Code selection input of audio data output, 2's compliment output when "L", offset binary
58	SLOB	1	output when "H".
59	PSSL	1	Mode selection input of audio data output, serial output when "L", parallel output when "H".
60	APTR	0	Control output for aperture compensation, "H" when R-ch.
61	APTL	0	Control output for aperture compensation, "H" when L-ch.
62	DA01	0	DA01 output (LSB of parallel audio data) when PSSL= "H", C1F1 output when PSSL= "L".
63	DA02	0	DA02 output when PSSL= "H", C1F2 output when PSSL= "L".
64	DA03	0	DA03 output when PSSL= "H", C2F1 output when PSSL= "L".
65	DA04	0	DA04 output when PSSL= "H", C2F2 output when PSSL= "L".
66	DA05	0	DA05 output when PSSL= "H", C2FL output when PSSL= "L".
67	DA06	0	DA06 output when PSSL= "H", C2PO output when PSSL= "L".
68	DA07	0	DA07 output when PSSL= "H", RFCK output when PSSL= "L".
69	DA08	0	DA08 output when PSSL= "H", WFCK output when PSSL= "L".
70	DA09	0	DA09 output when PSSL= "H", PLCK output when PSSL= "L".
71	DA10	0	DA10 output when PSSL= "H", UGFS output when PSSL= "L".
72	DA11	0	DA11 output when PSSL= "H", GTOP output when PSSL= "L".
73	VDD	-	Power supply (+5V)
74	DA12	0	DA12 output when PSSL= "H", RAOV output when PSSL= "L".
75	DA13	0	DA13 output when PSSL= "H", C4LR output when PSSL= "L".
76	DA14	0	DA14 output when PSSL= "H", BLCK output when PSSL= "L".
77	DA15	0	DA15 output when PSSL= "H", BLCK output when PSSL= "L".
78	DA16	0	DA16 output (MSB of parallel audio data) when PSSL= "H", data output when PSSL= "L".
79	WDCK	0	Strobe signal output, 88.2 kHz.
80	LRCK	0	Strobe signal output, 44.1 kHz.



## 10. PANEL FACILITIES



### FRONT PANEL

- POWER switch/indicator
   Press to turn power ON and OFF.
- ② Disc tray
- 3 Remote sensor
- ④ PLAY button/indicator (▷)
- ⑤ TRACK search buttons (►
- ⑥ STOP button (■)
- ⑦ OPEN/CLOSE button (♠)
- **8 OUTPUT SELECTOR button/indicators**
- DISPLAY OFF button/indicator
- PAUSE button/indicator

### REMOTE CONTROL UNIT

Buttons listed here but not accompanied with explanations have the same functions as the corresponding front-panel buttons. If use is made of the supplied remote control unit, remote operation is possible.

To use the remote control unit, aim at the remote sensor. The remote control unit can operate over a range of approximately 23 feet (7 meters), within angles of 30 degrees left and right.

### NOTE

If the remote control sensor window is in a position where it receives strong light such as sunlight or fluorescent light, control may not be possible.

- (1) OPEN/CLOSE button
- ① INDEX search buttons ( ←, →)
- (13) MANUAL search buttons (◄◄, ▶▶)
- 14 PAUSE button
- 15 Track number buttons  $(1-10, +10, \ge 20)$
- 16 CHECK button
- (7) PROGRAM button
- (18) TIME button
- (19) REPEAT button
- 20 RANDOM PLAY button
- ② TRACK search buttons (◄◄, ▶►)
- 22 PLAY button
- 23 STOP button
- 24 CLEAR button

### **SPECIFICATIONS** 11.

Type ...... Compact disc digital audio system Usable discs ...... Compact Disc Power requirements U.K. and Australian models ...... AC 240V, 50/60Hz European model ...... AC 220V, 50/60Hz U.S. and Canadian models ...... AC 120V, 60Hz Multi-voltage model ...... AC 110/120-127/220/240V (switchable) 50/60Hz Power consumption ...... 30W Operating temperature ...... +5°C-+35°C (+41°F-+95°F) Weight ...... 15.2kg (33lb, 8oz) 18-1/16(W) × 17-1/8(D) × 5-15/16(H) in.

### 2. Audio section

Frequency response	2Hz – 20kHz
S/N	. 115dB or more (EIAJ)
Dynamic range	
Channel separation	. 110dB or more (EIAJ)
Total harmonic distortion	0.0015% or less (EIAJ)
Output voltage	2.0V
Wow and flutter	. Limit of measurement
	W.PEAK) or less (EIAJ)
Number of channels	2 channels (stereo)

### 3. Output terminal

- Audio line output terminals
- Optical digital output terminal
- Coaxial digital output terminal

### 4. Functions

- Play
- Pause
- Stop
- Track search
- Manual search
- Index search
- Direct selection
- Single track repeat
- All track repeat
- Programmed repeat
- Random play repeat
- Programmed random play repeat
- Programmed playback (up to 24 steps)
- Pause program
- Program check
- Program correction
- Program clear
- Random play
- Programmed random play
- Time location
- Display off
- Timer start
- Digital/analog output select

### Accessories

•	Remote control unit	1
•	Size AAA/RO3 dry cell batteries	2
•	Output cable	1
	Operating instructions	

The specifications and design of this product are subject to change without notice, due to improvements.